

FIGURE 1

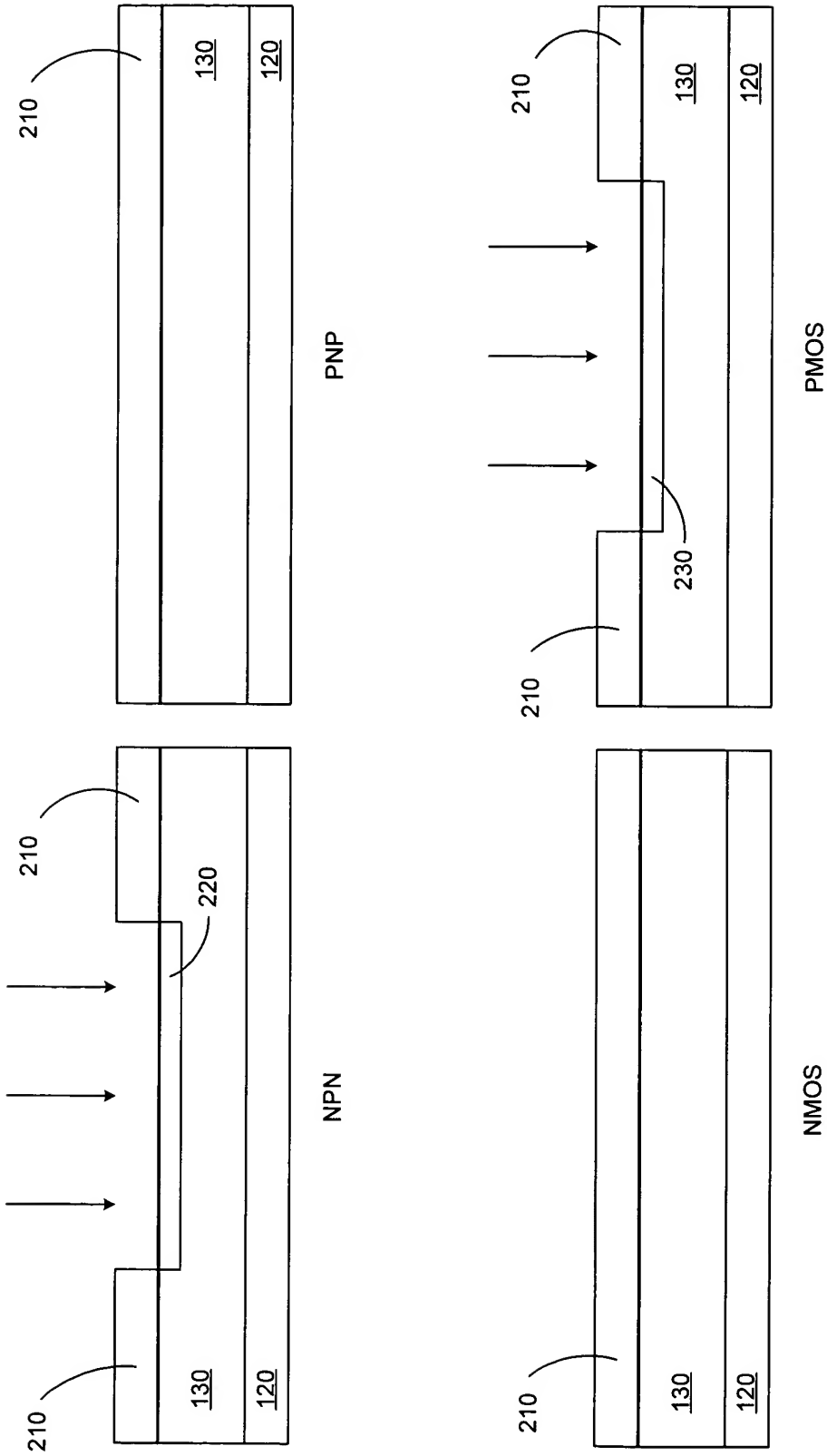
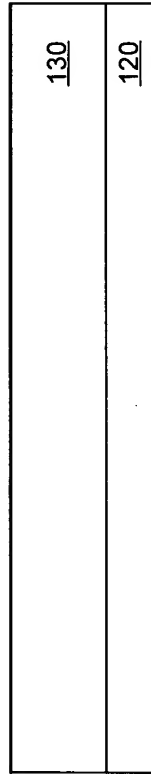
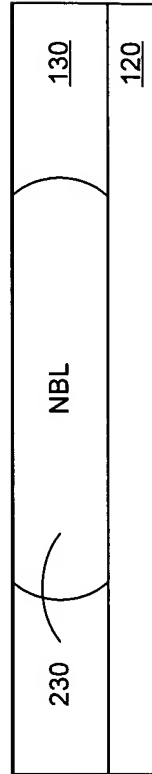


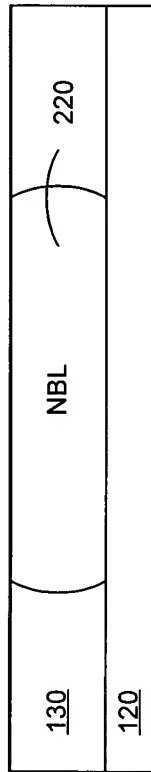
FIGURE 2



PNP



PMOS



NPN



NMOS

FIGURE 3

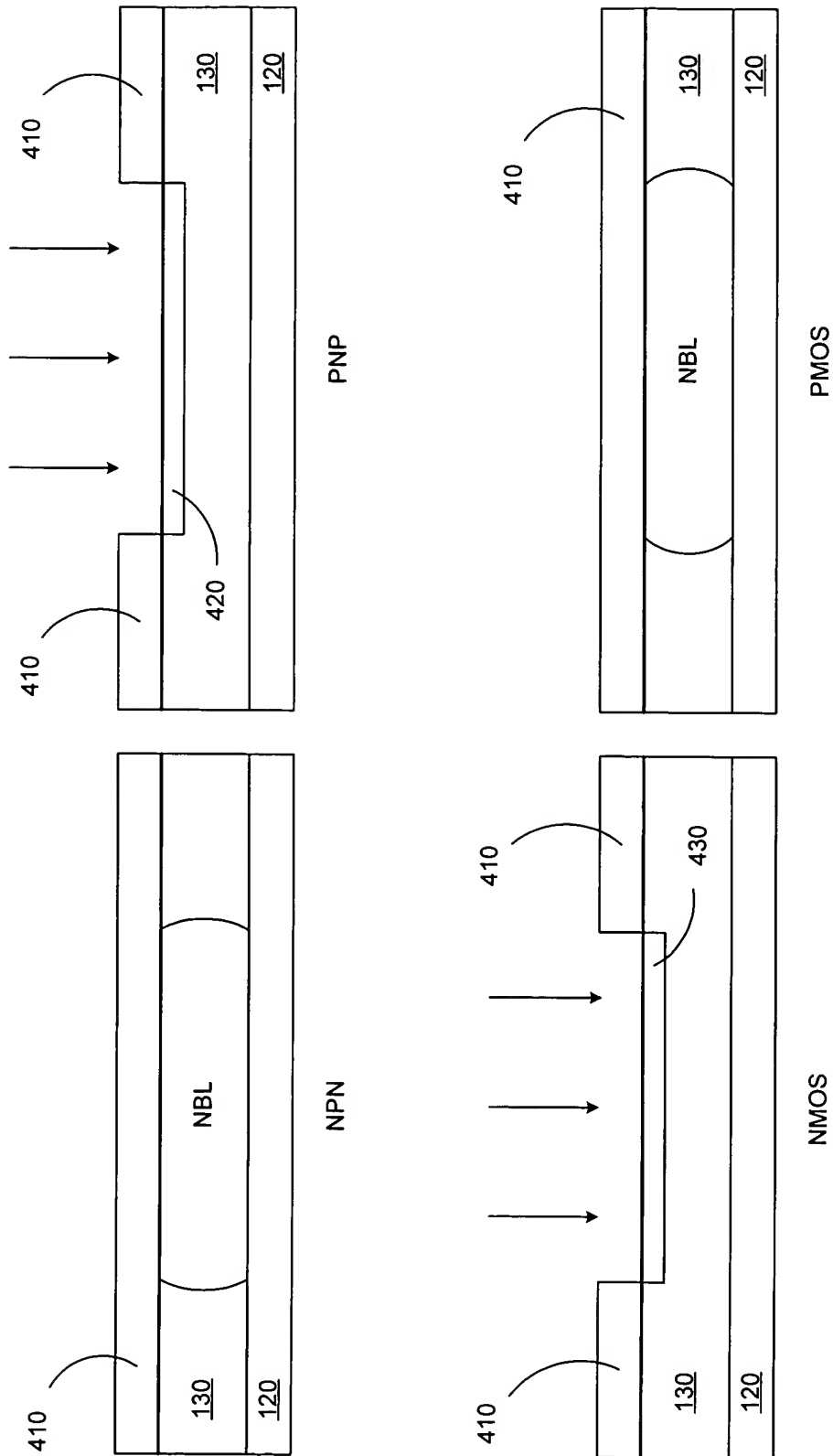
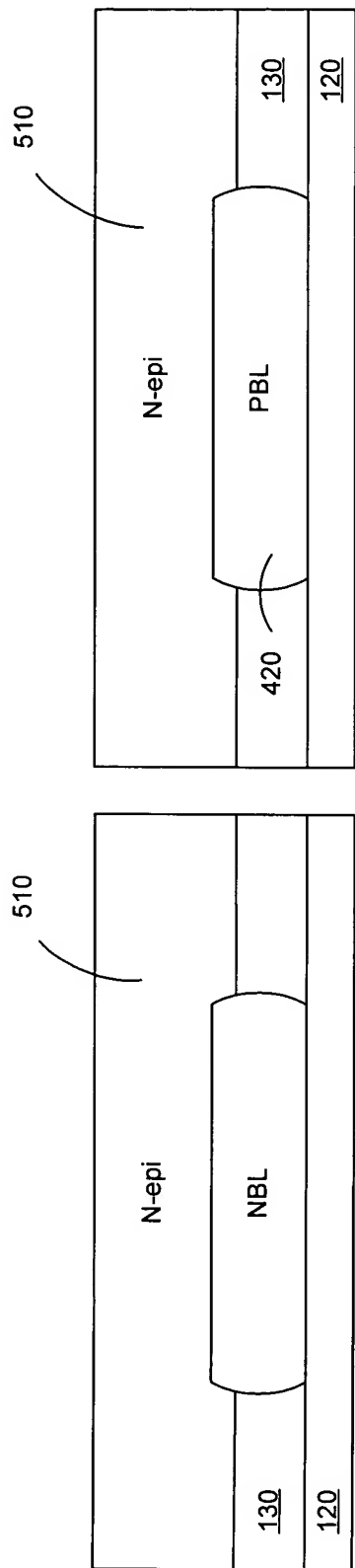
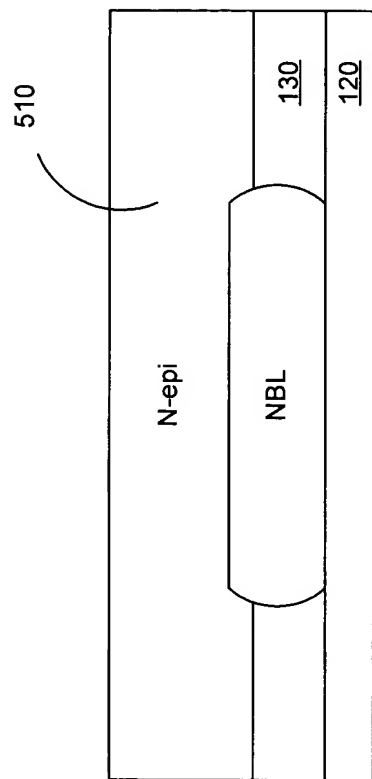


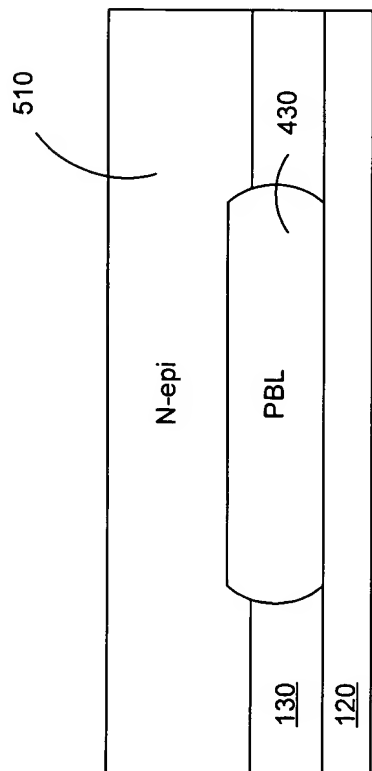
FIGURE 4



NPN



PMOS



NMOS

FIGURE 5

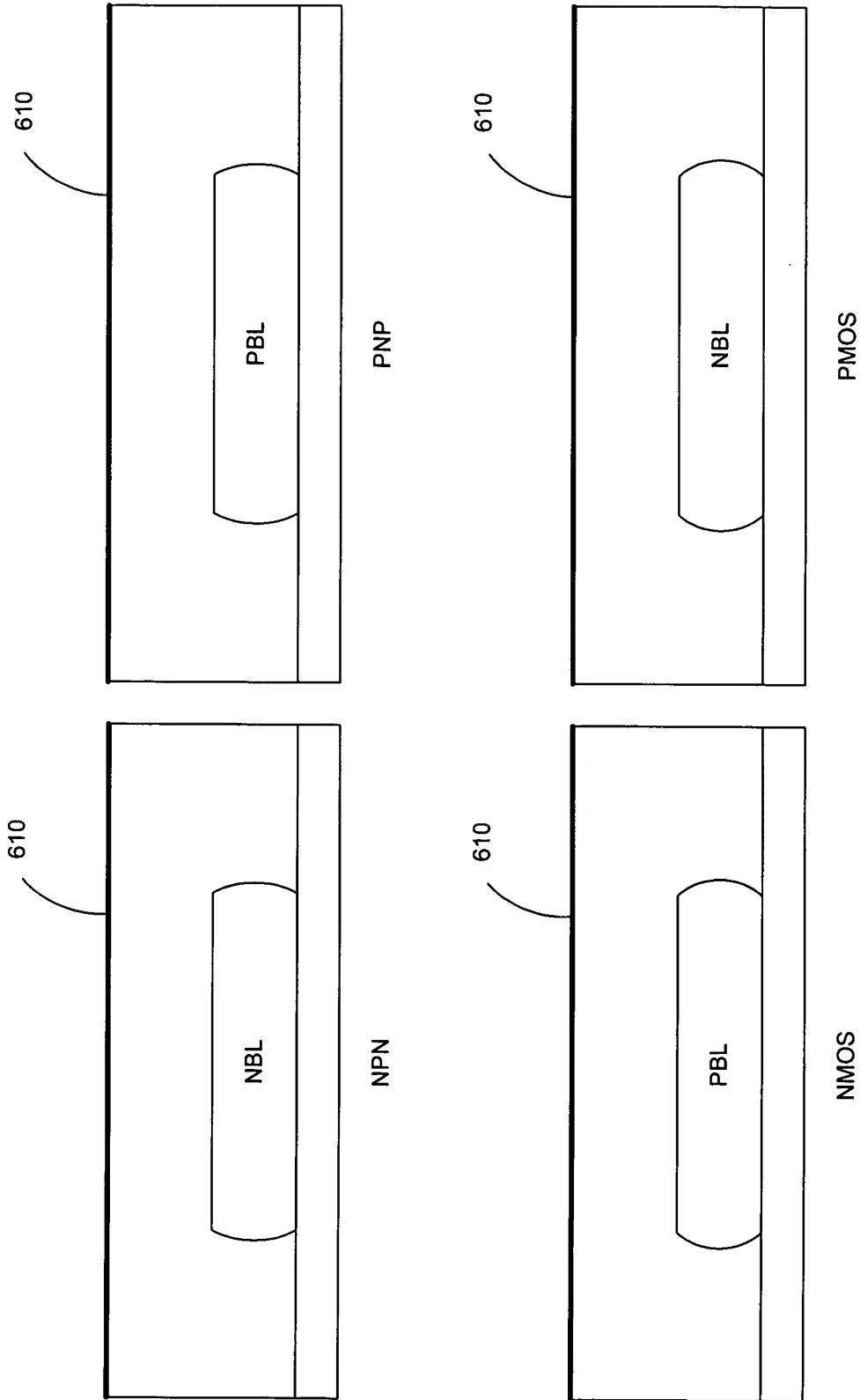


FIGURE 6

INFORMAL DRAWINGS

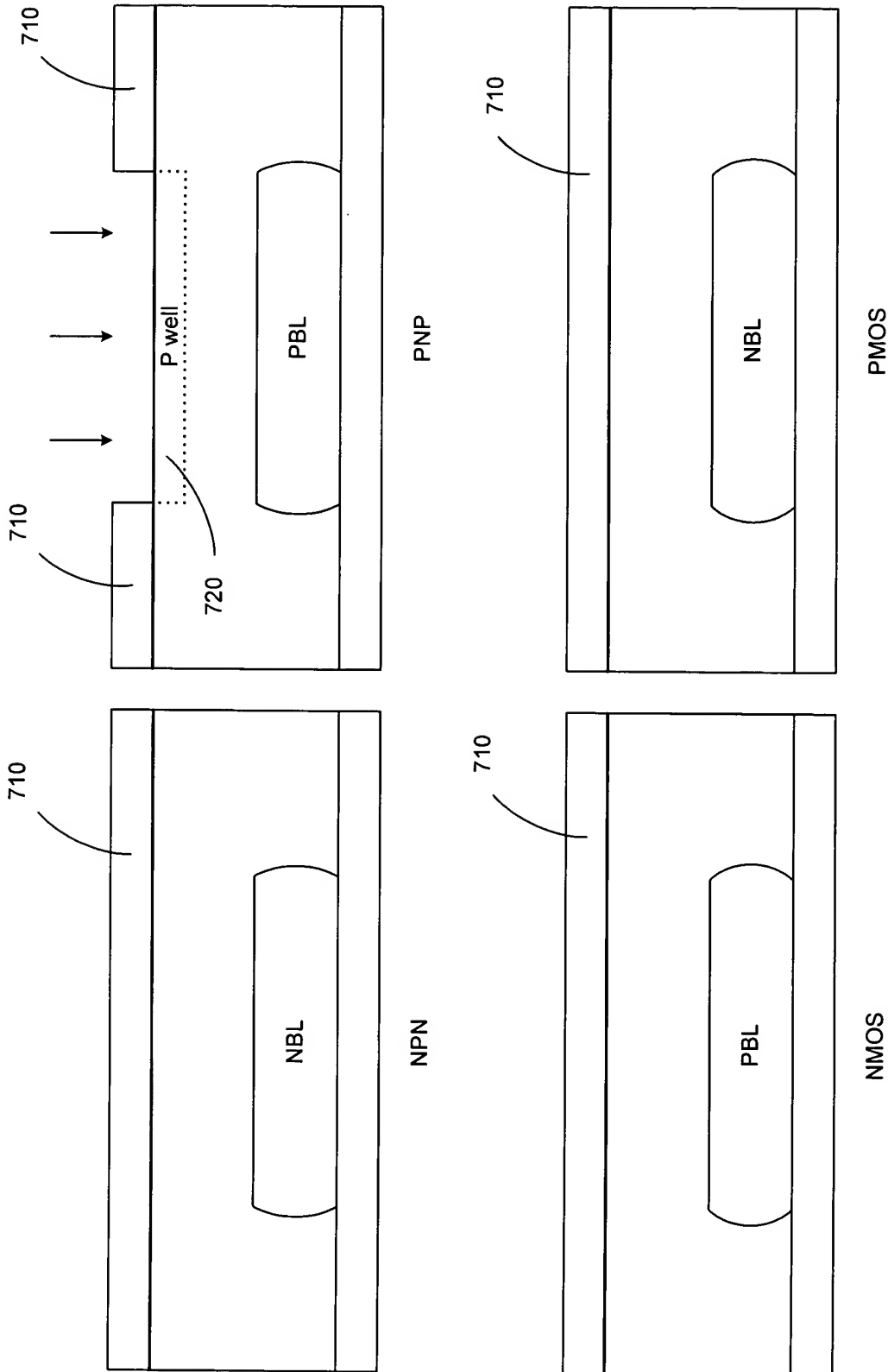


FIGURE 7

INFORMAL DRAWINGS

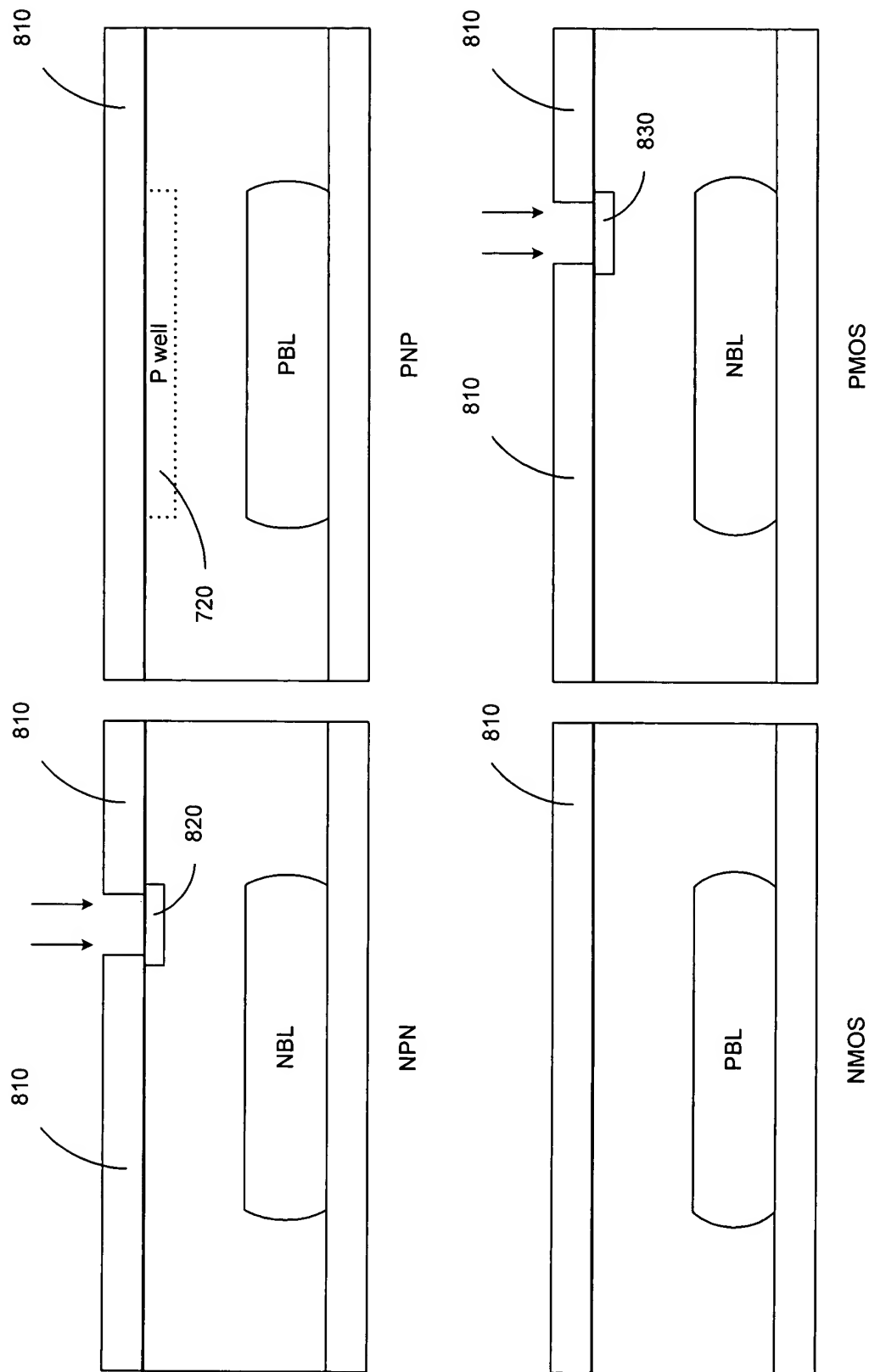


FIGURE 8

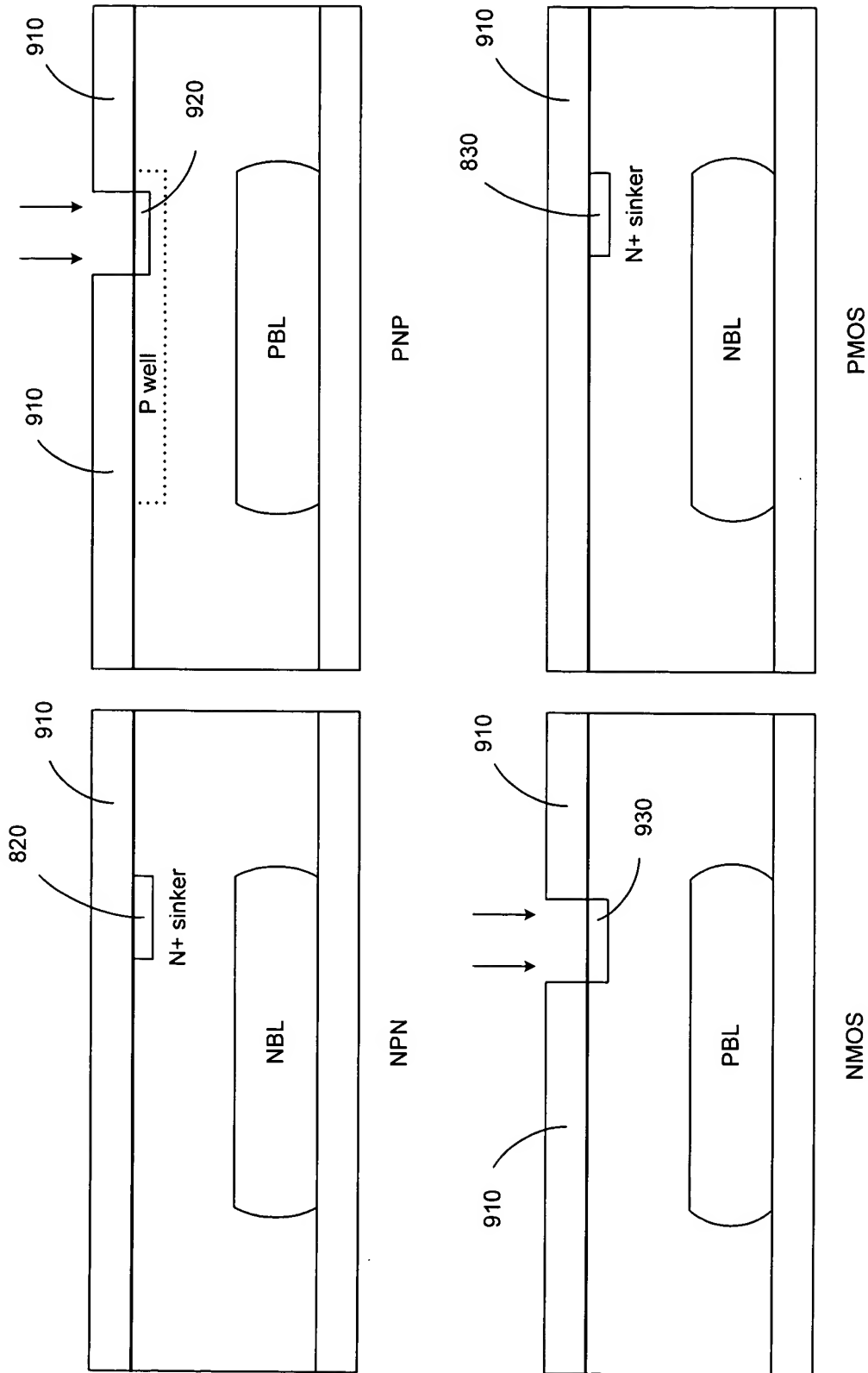


FIGURE 9

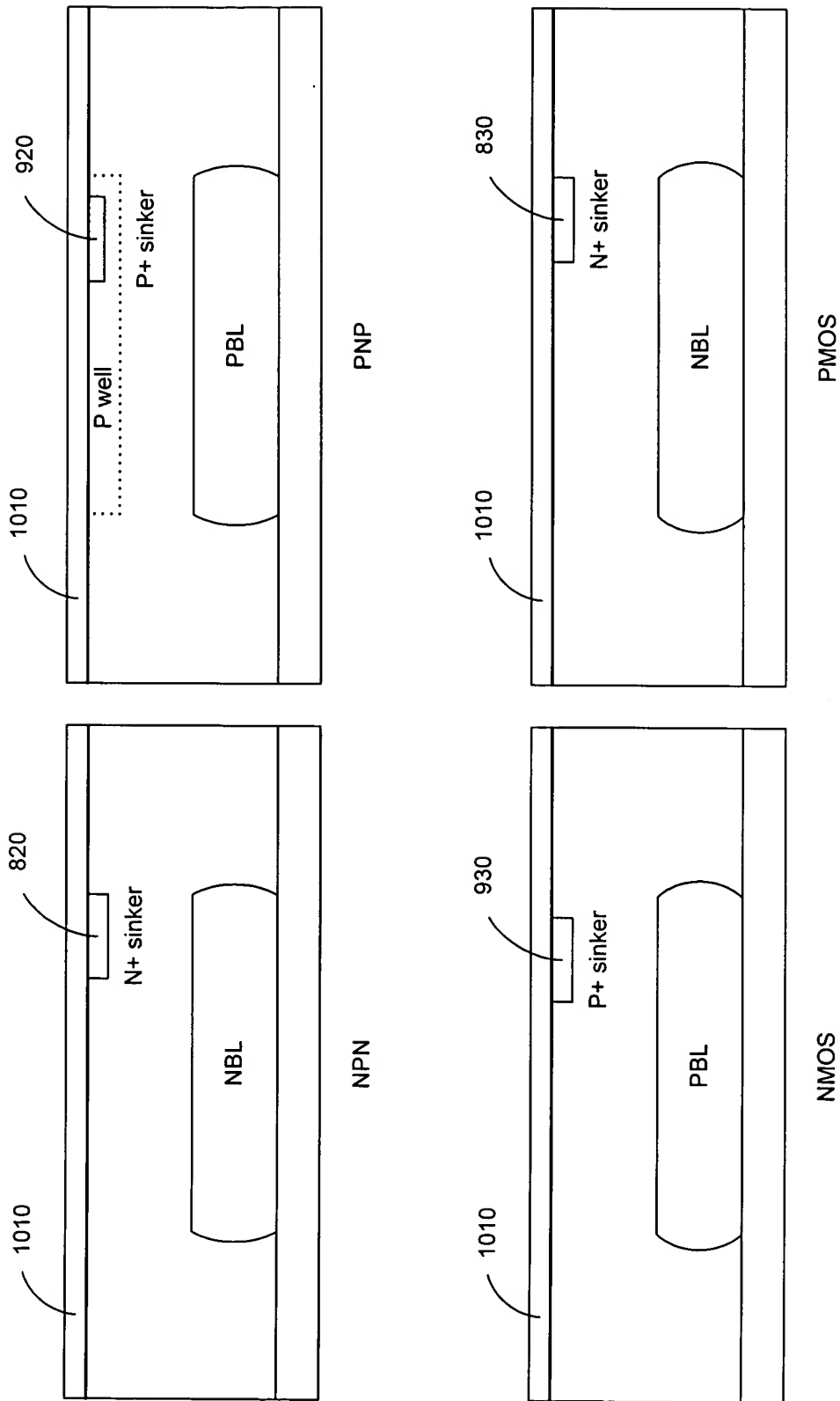
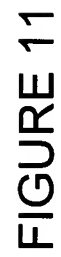
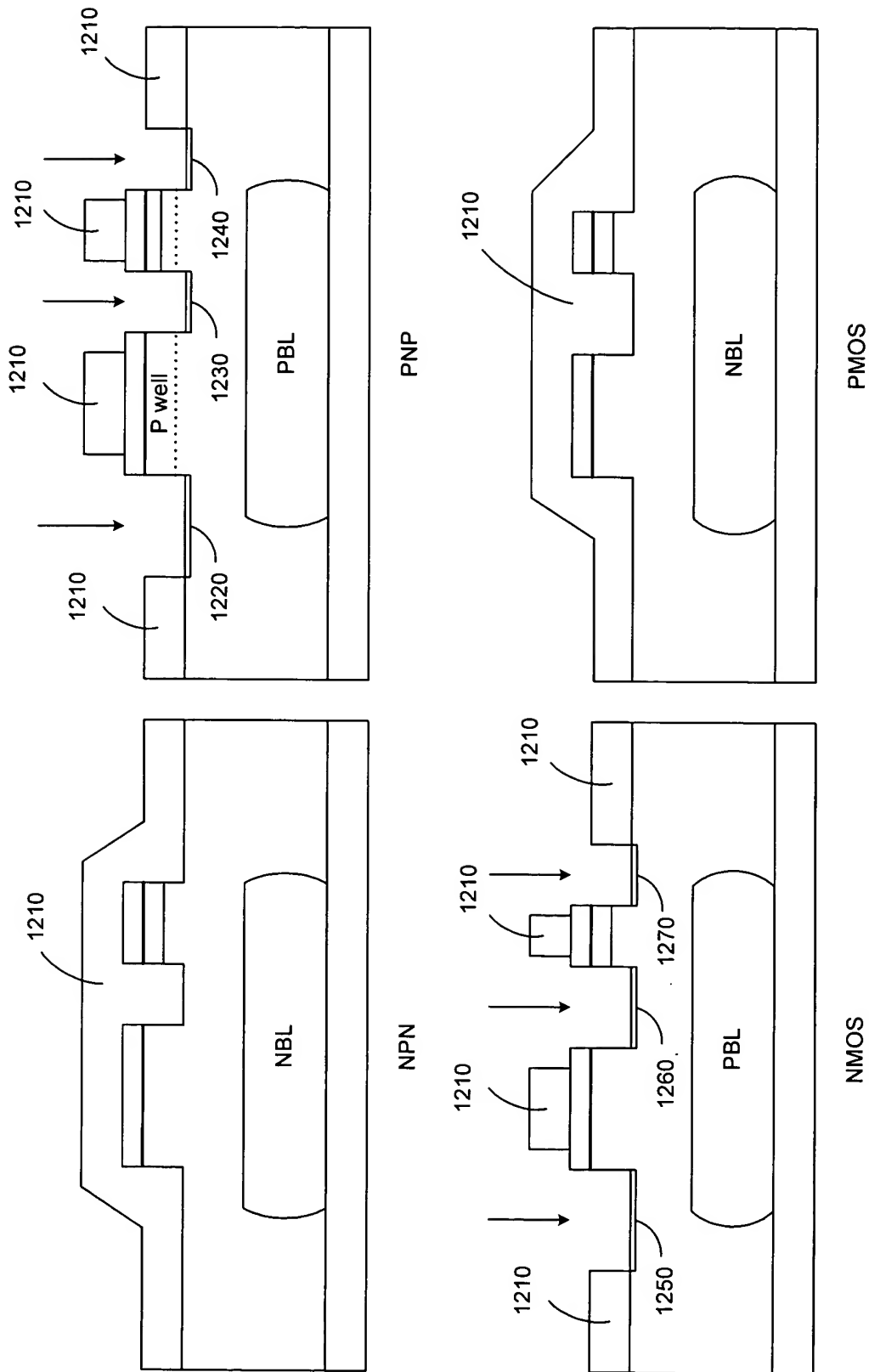


FIGURE 10





INFORMAL DRAWINGS

FIGURE 12

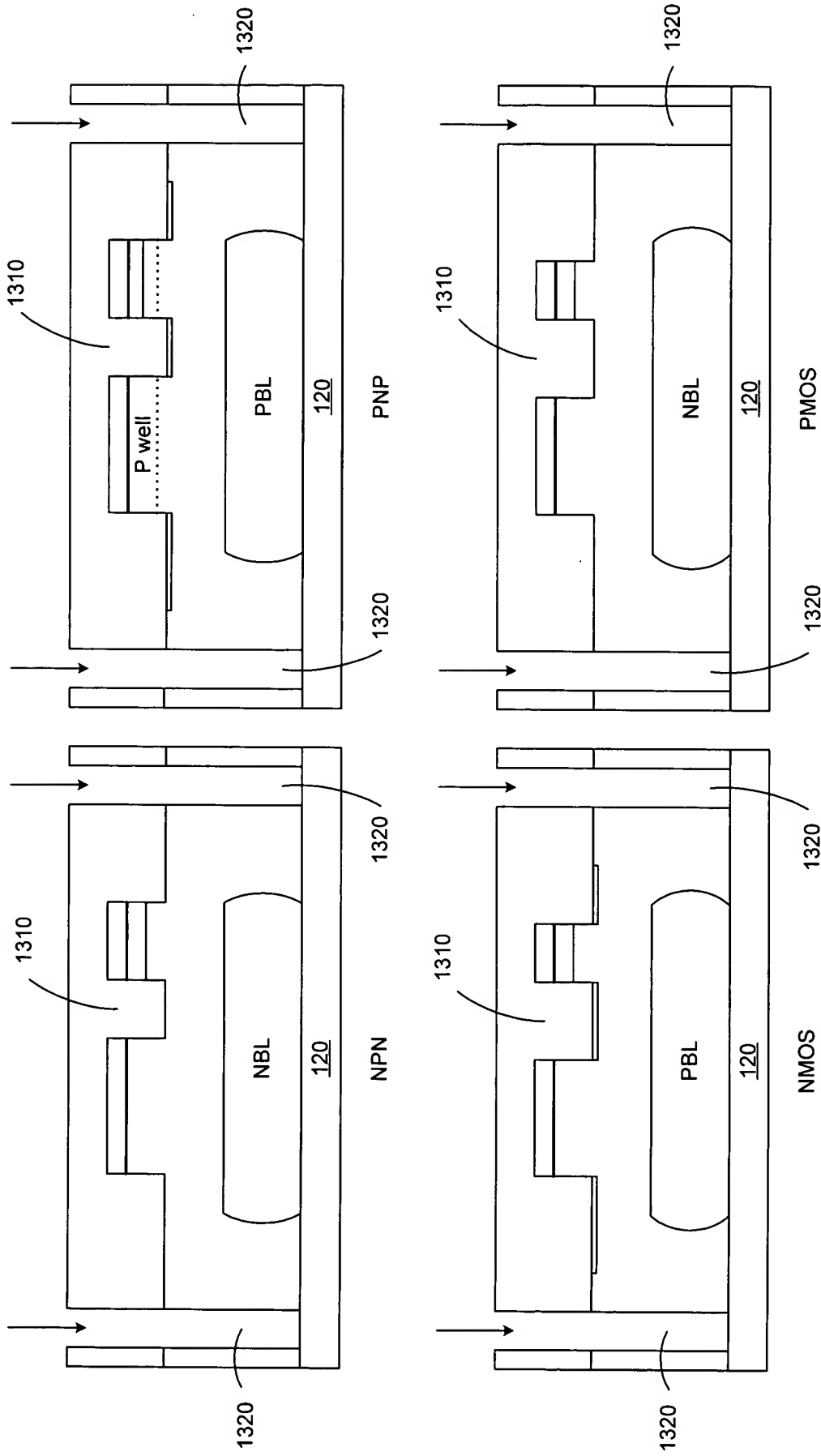


FIGURE 13

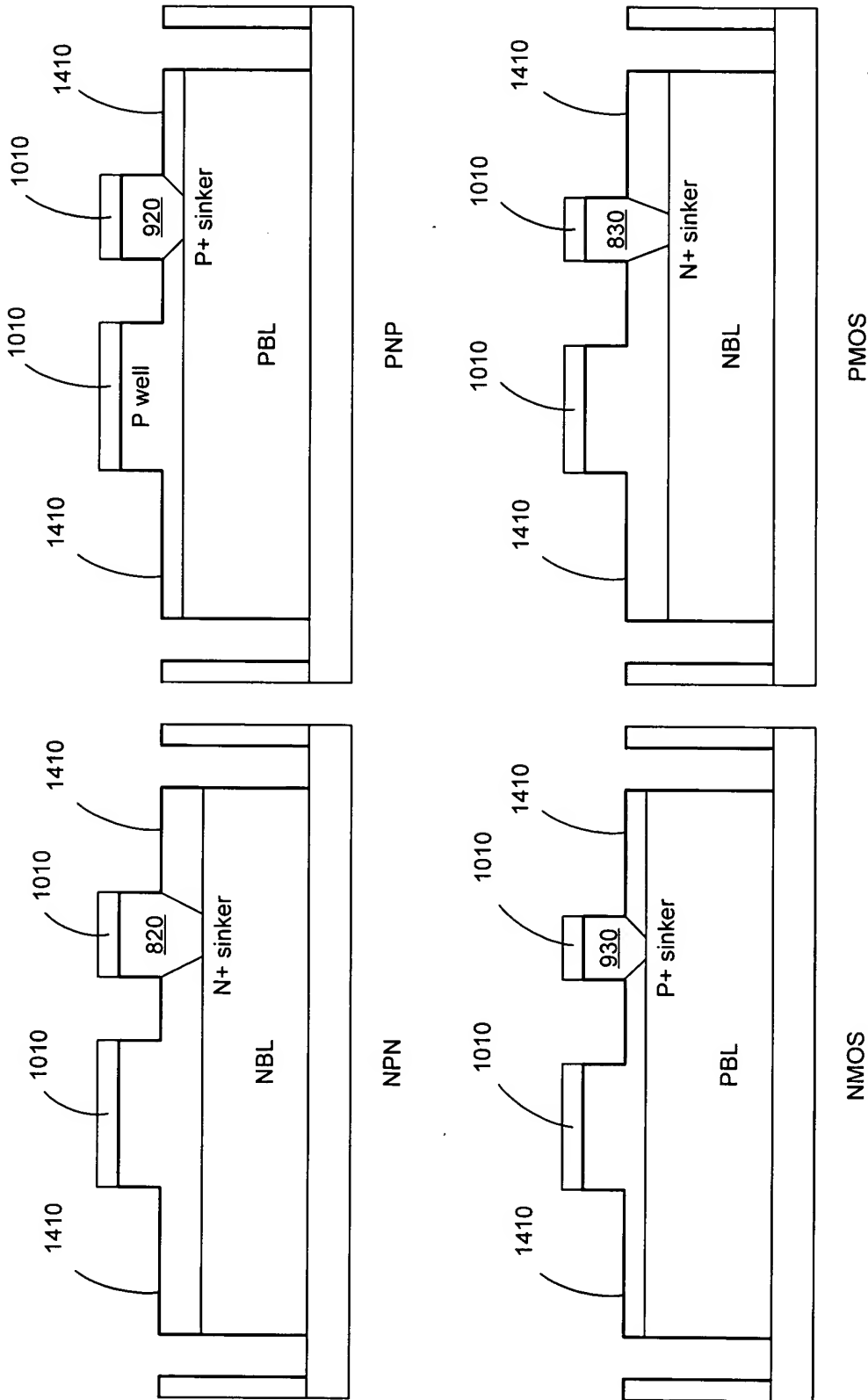


FIGURE 14

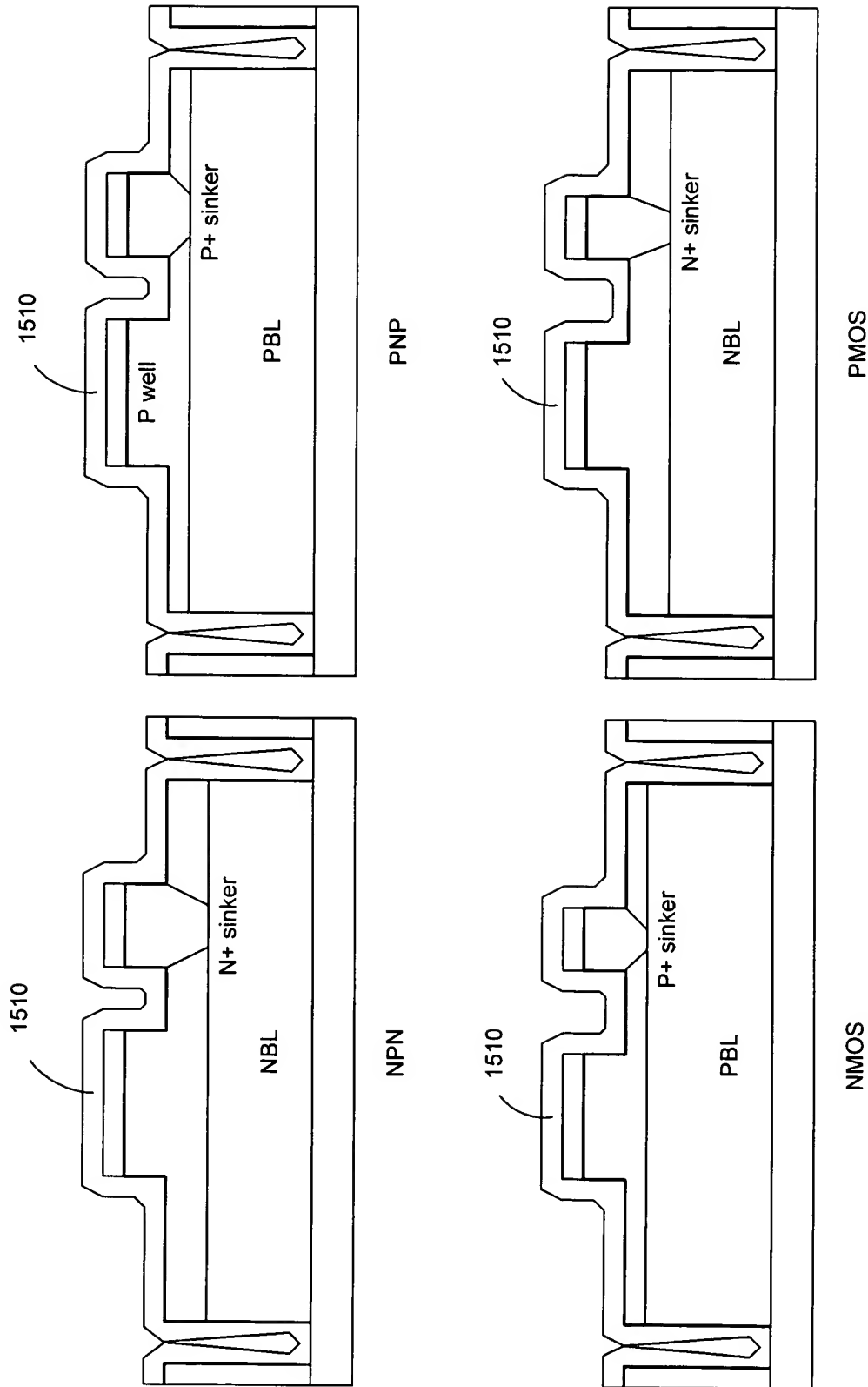


FIGURE 15

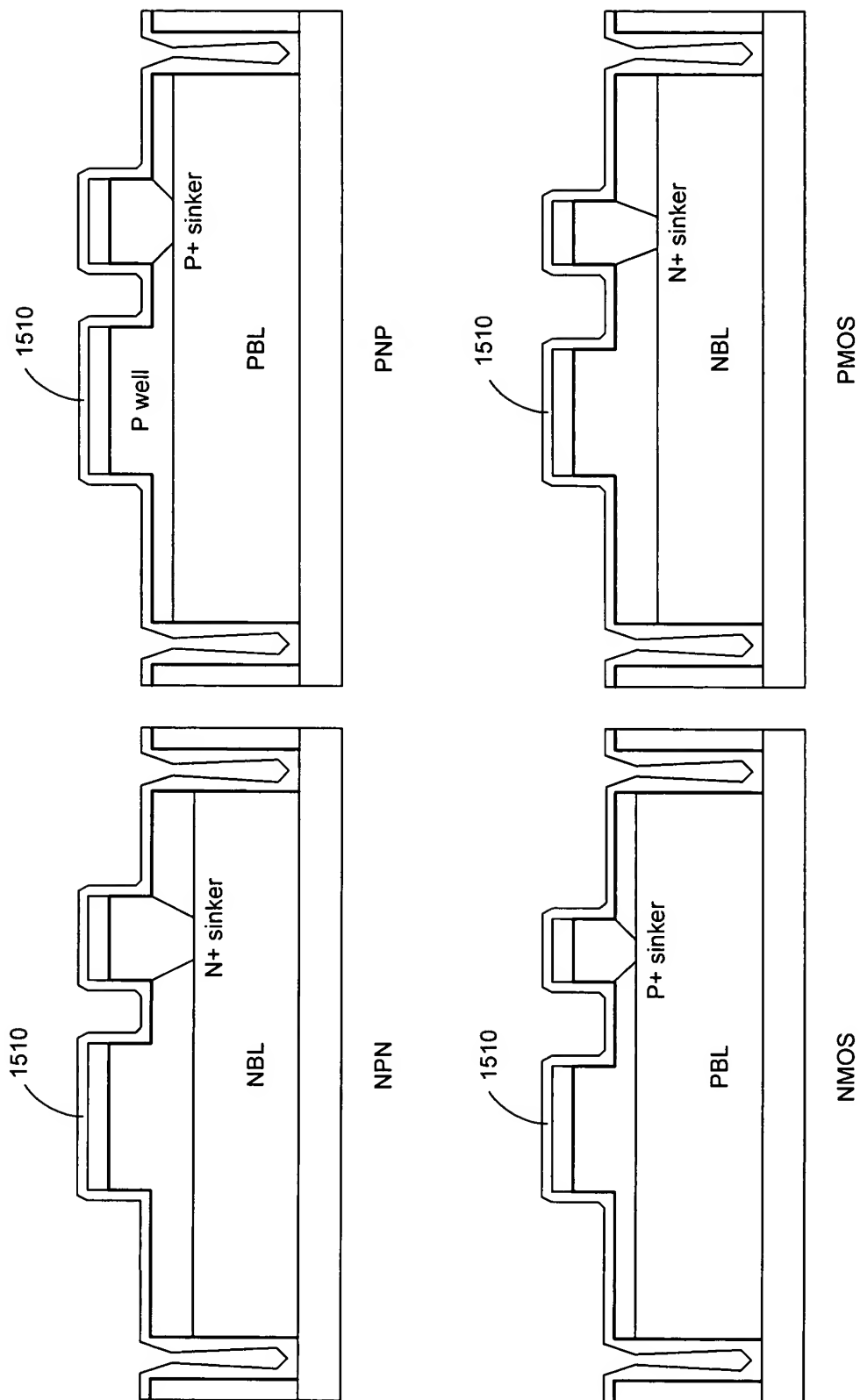


FIGURE 16

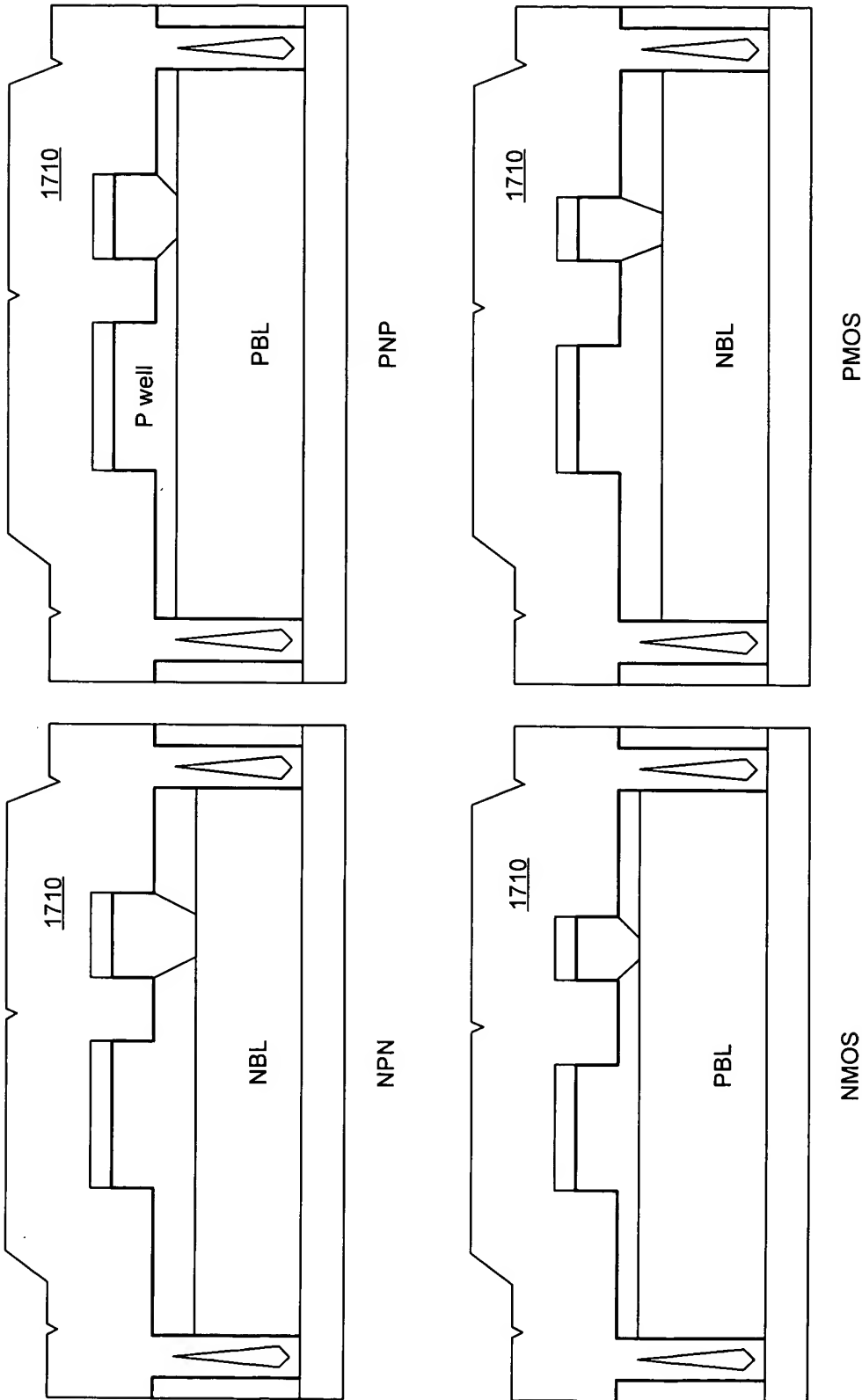


FIGURE 17

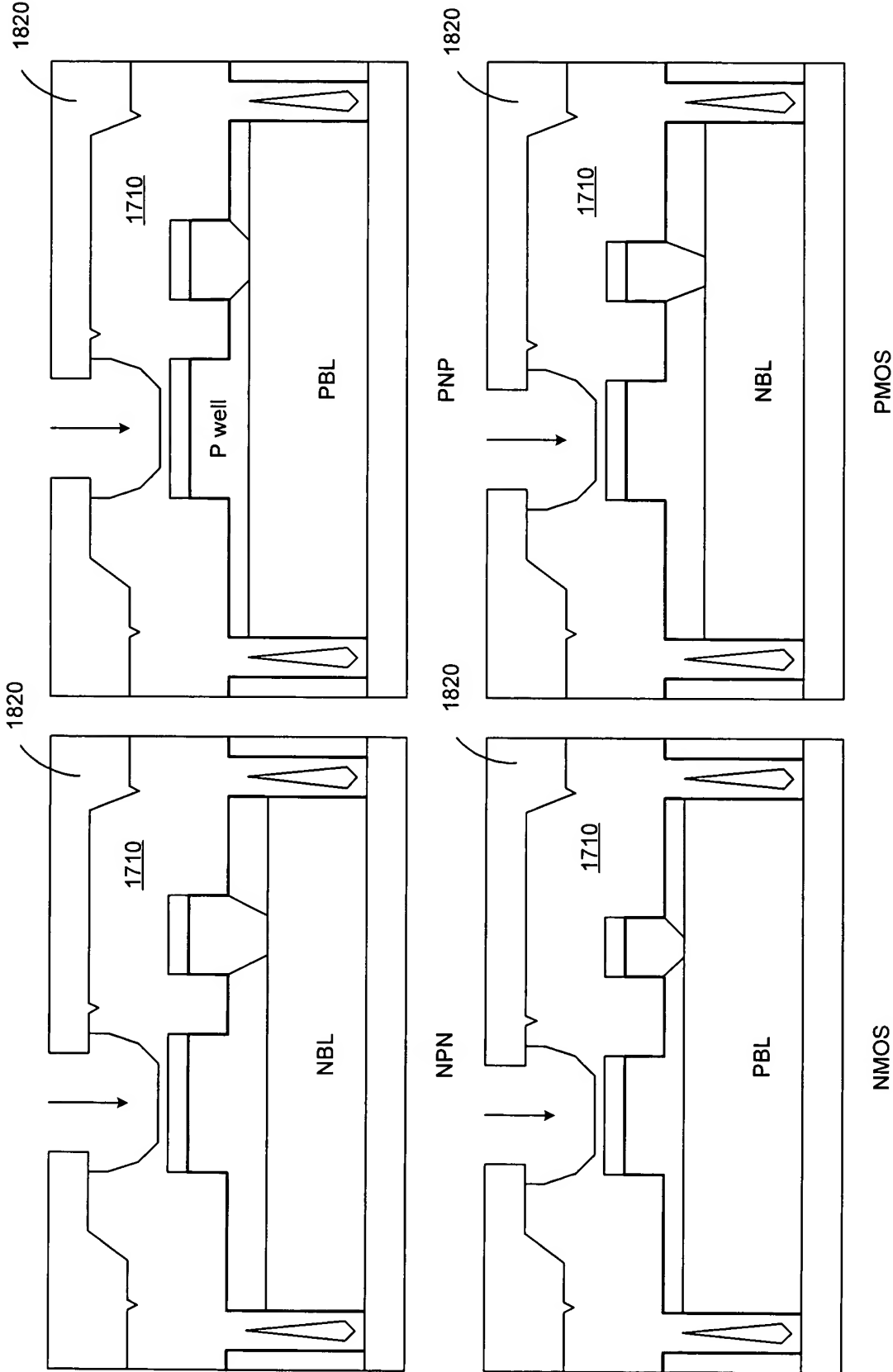


FIGURE 18

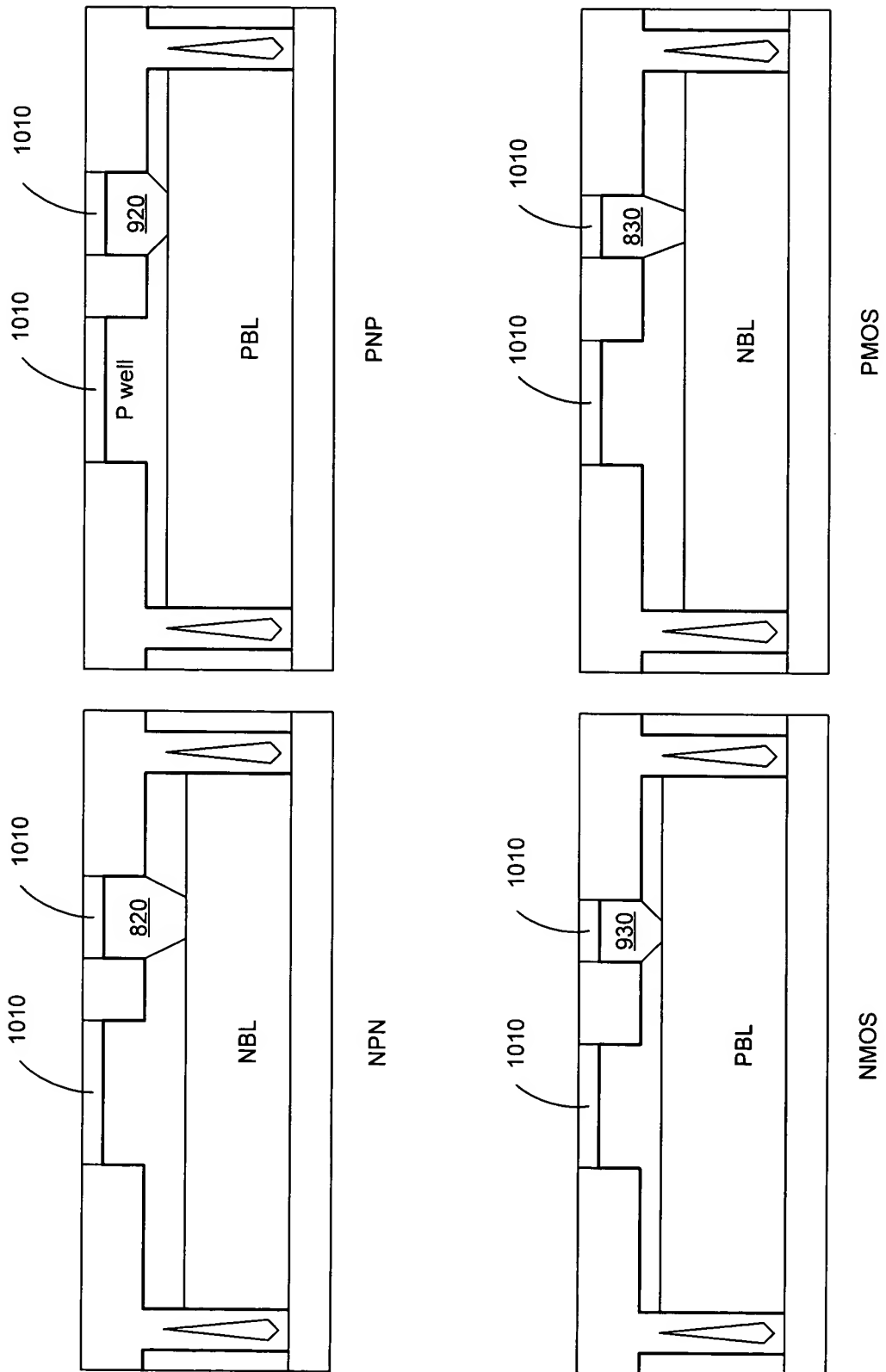
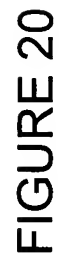


FIGURE 19



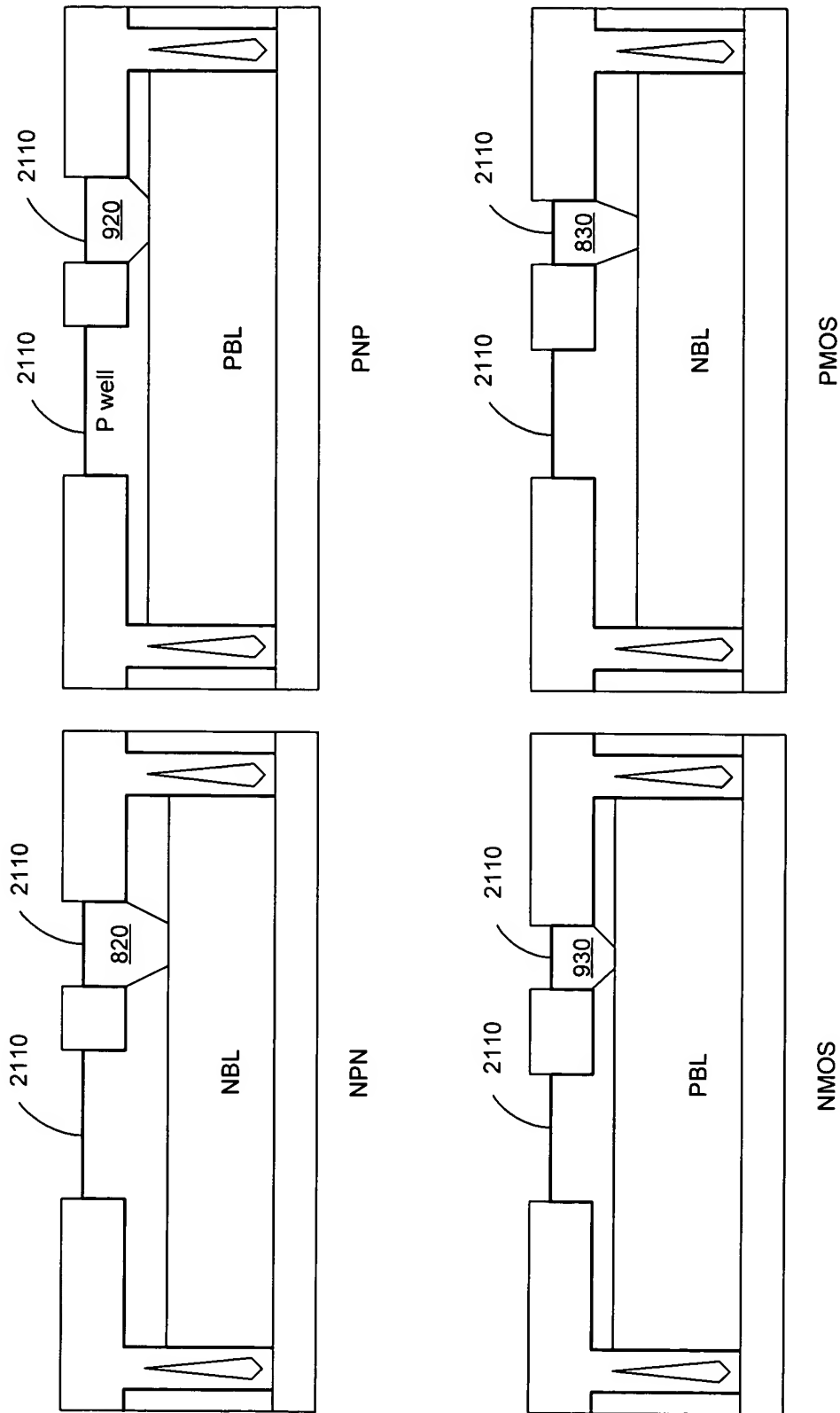


FIGURE 21

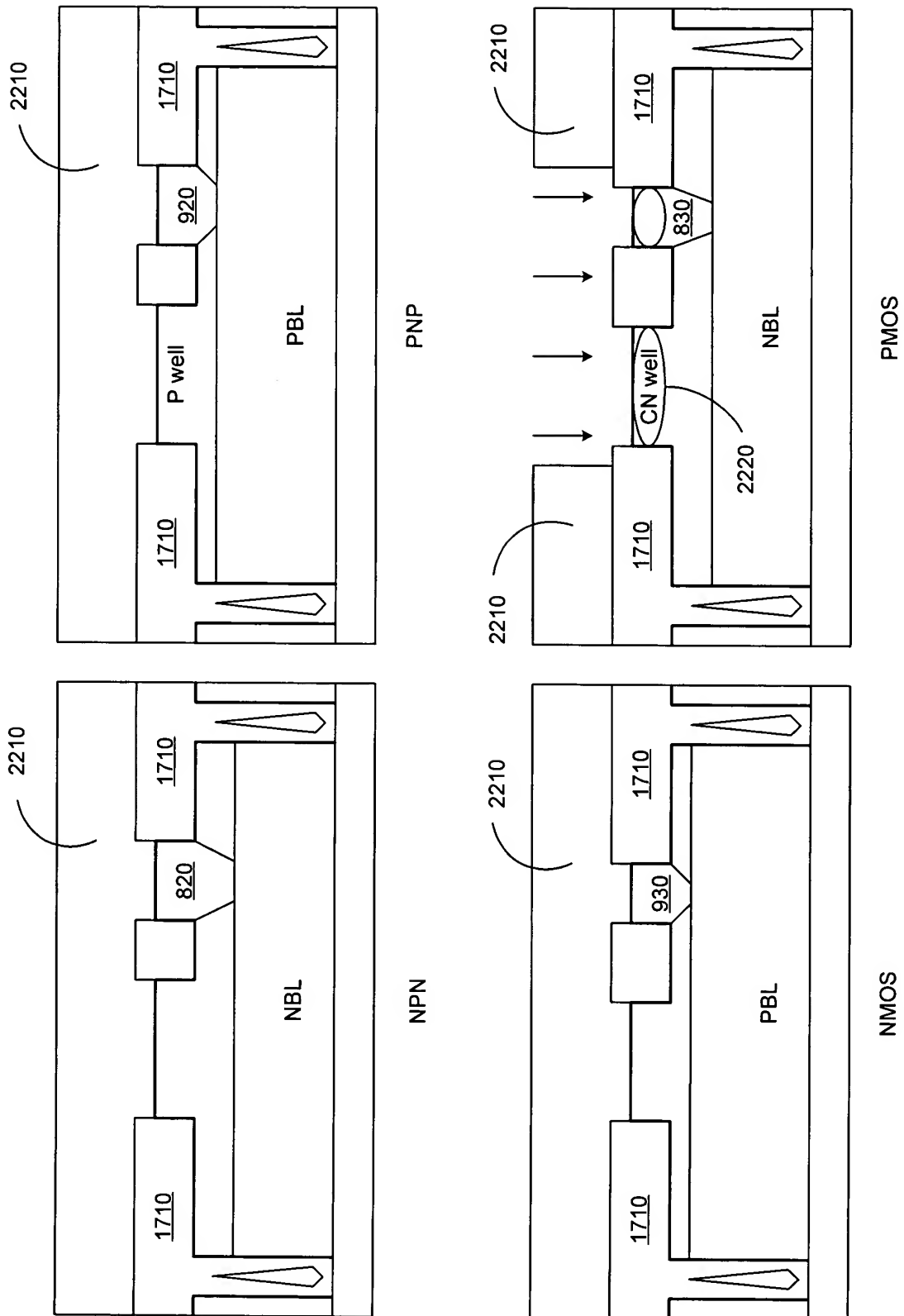


FIGURE 22

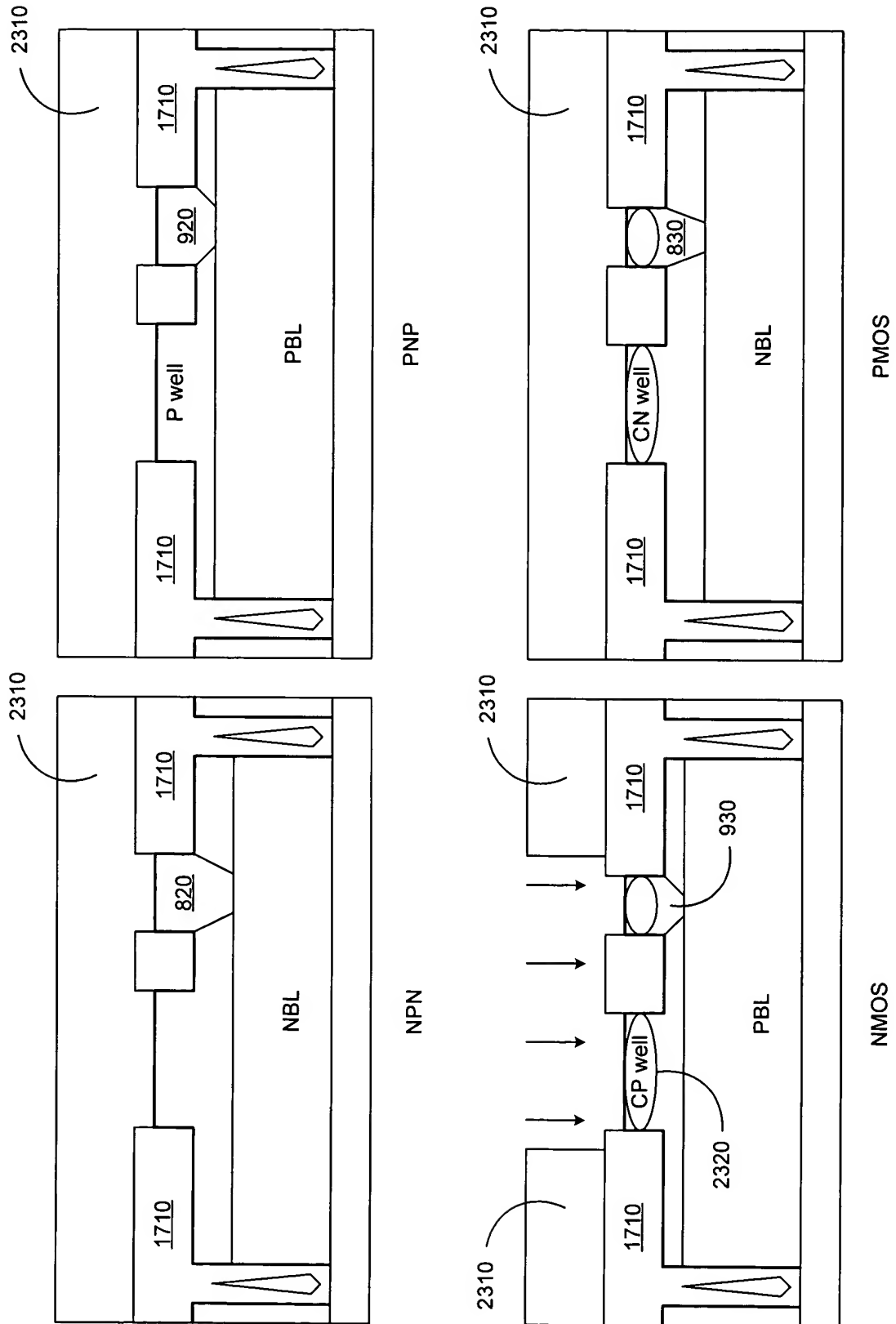


FIGURE 23

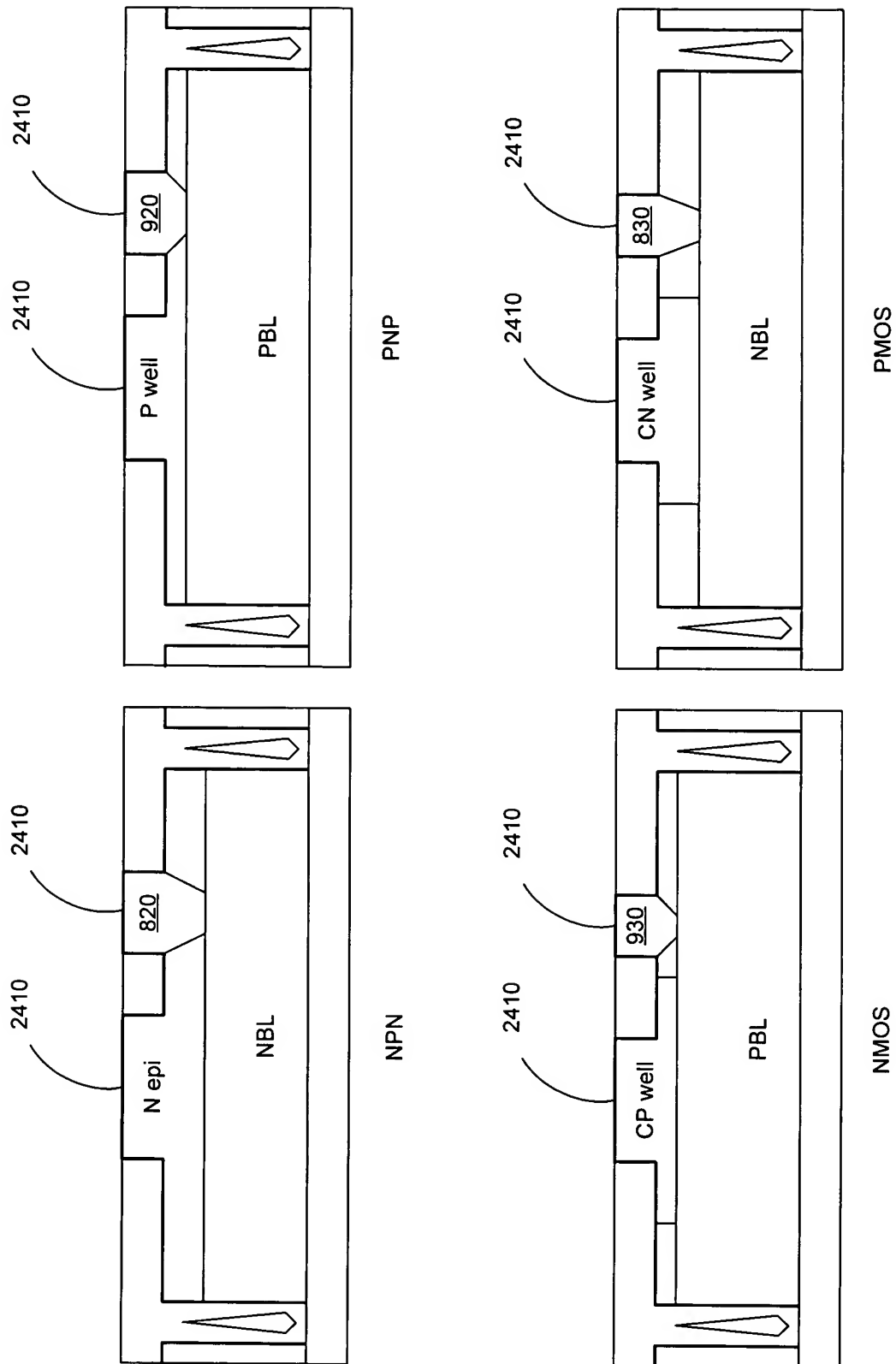


FIGURE 24

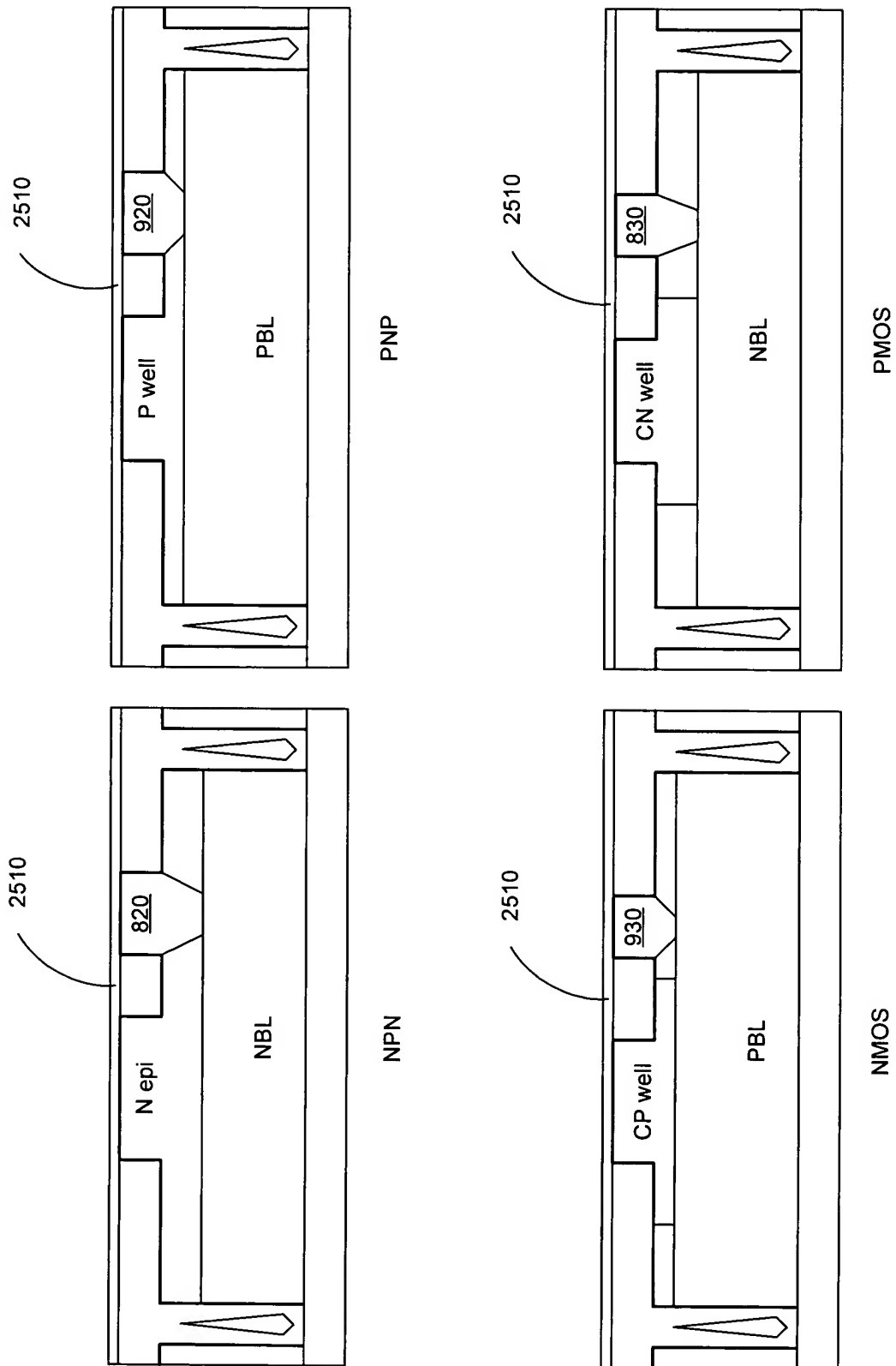


FIGURE 25

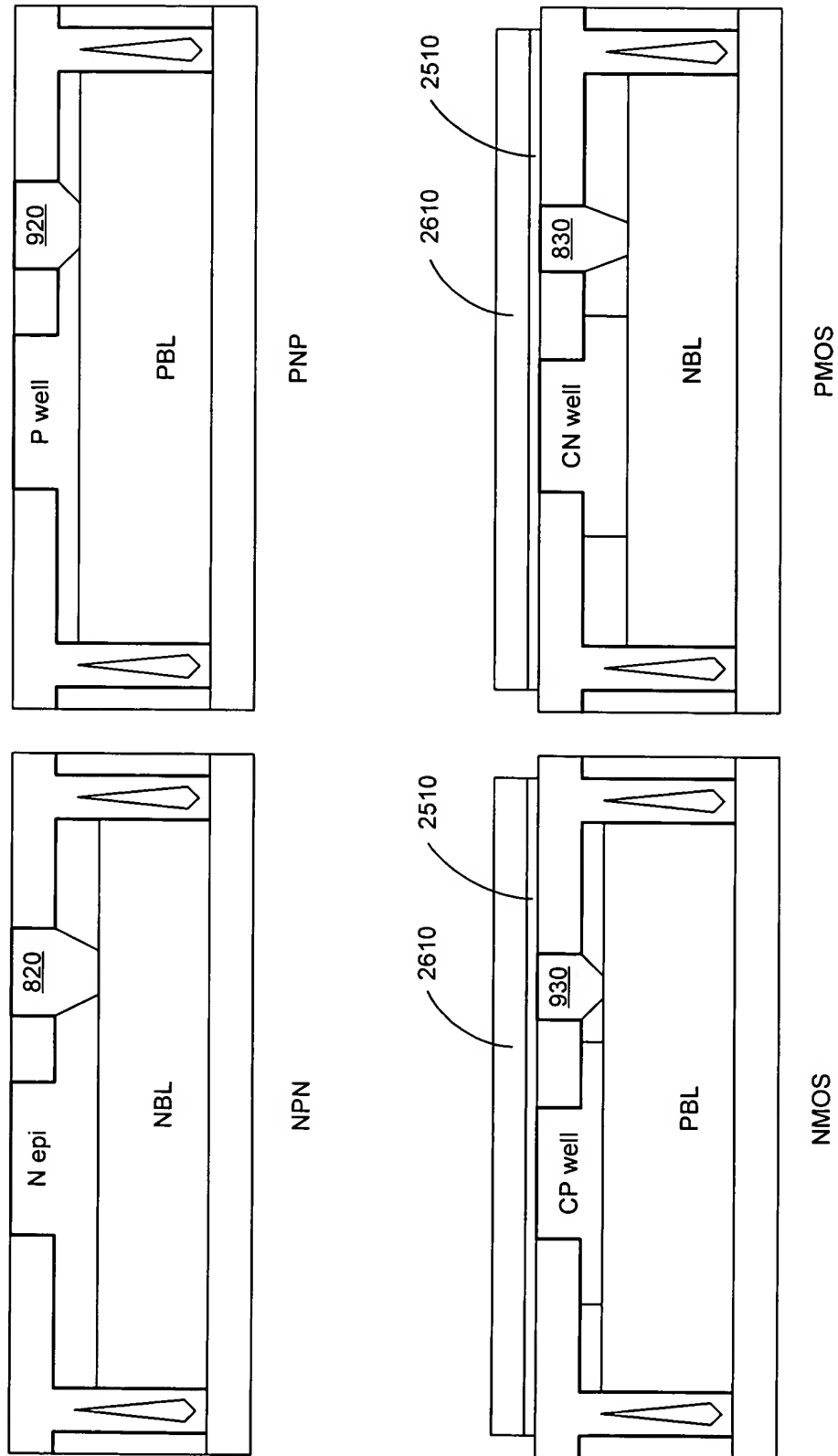


FIGURE 26

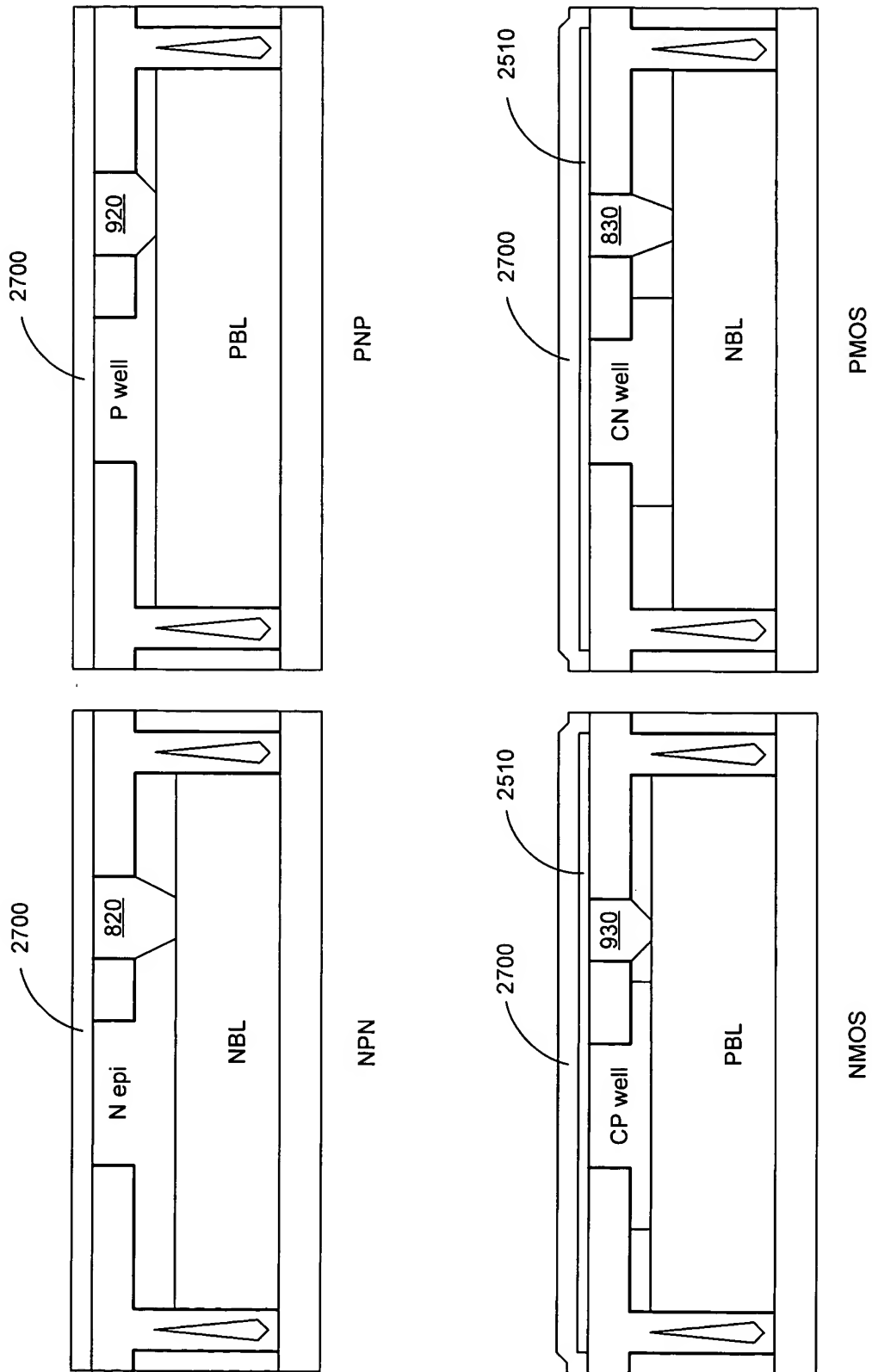


FIGURE 27

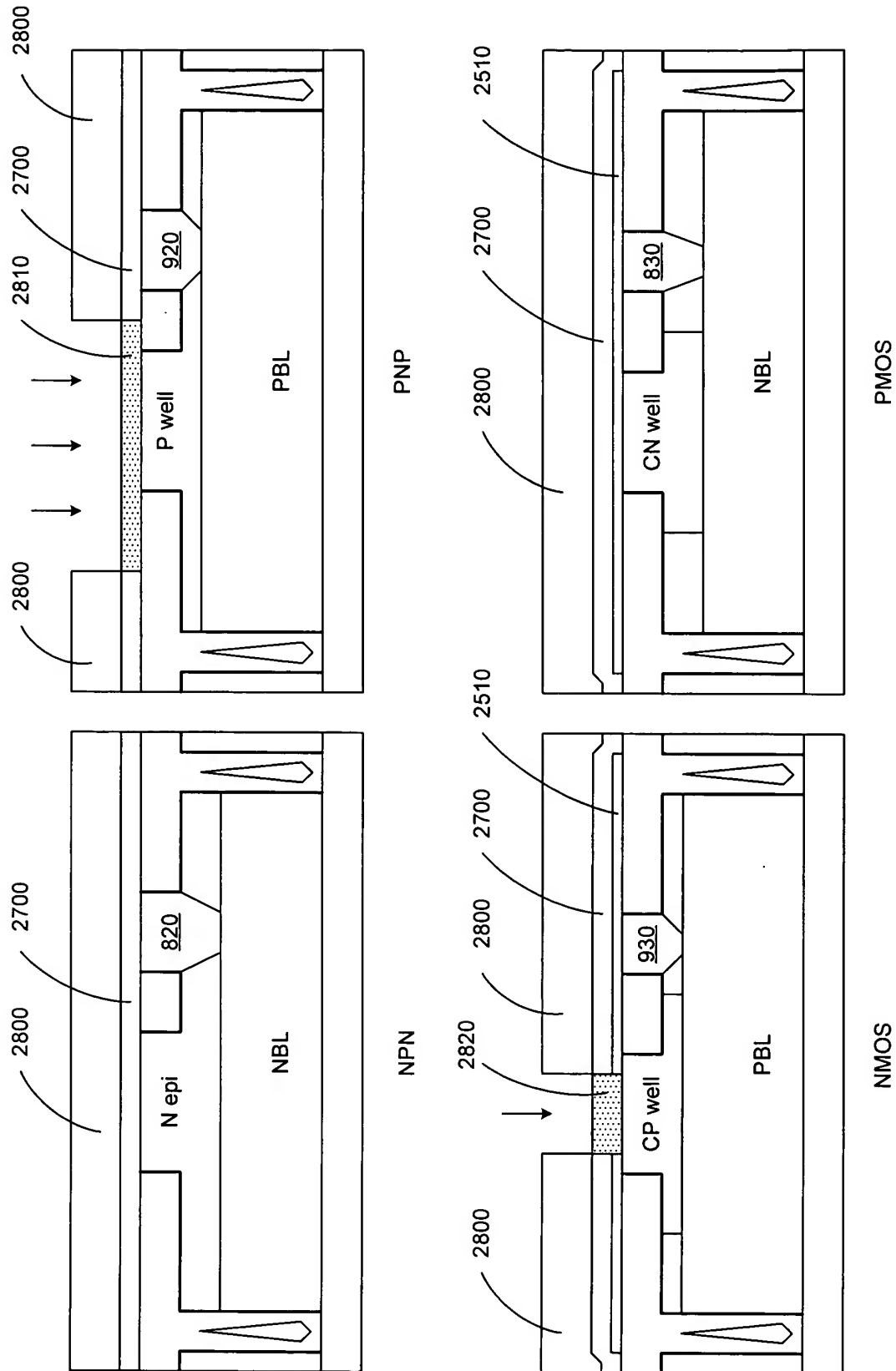


FIGURE 28

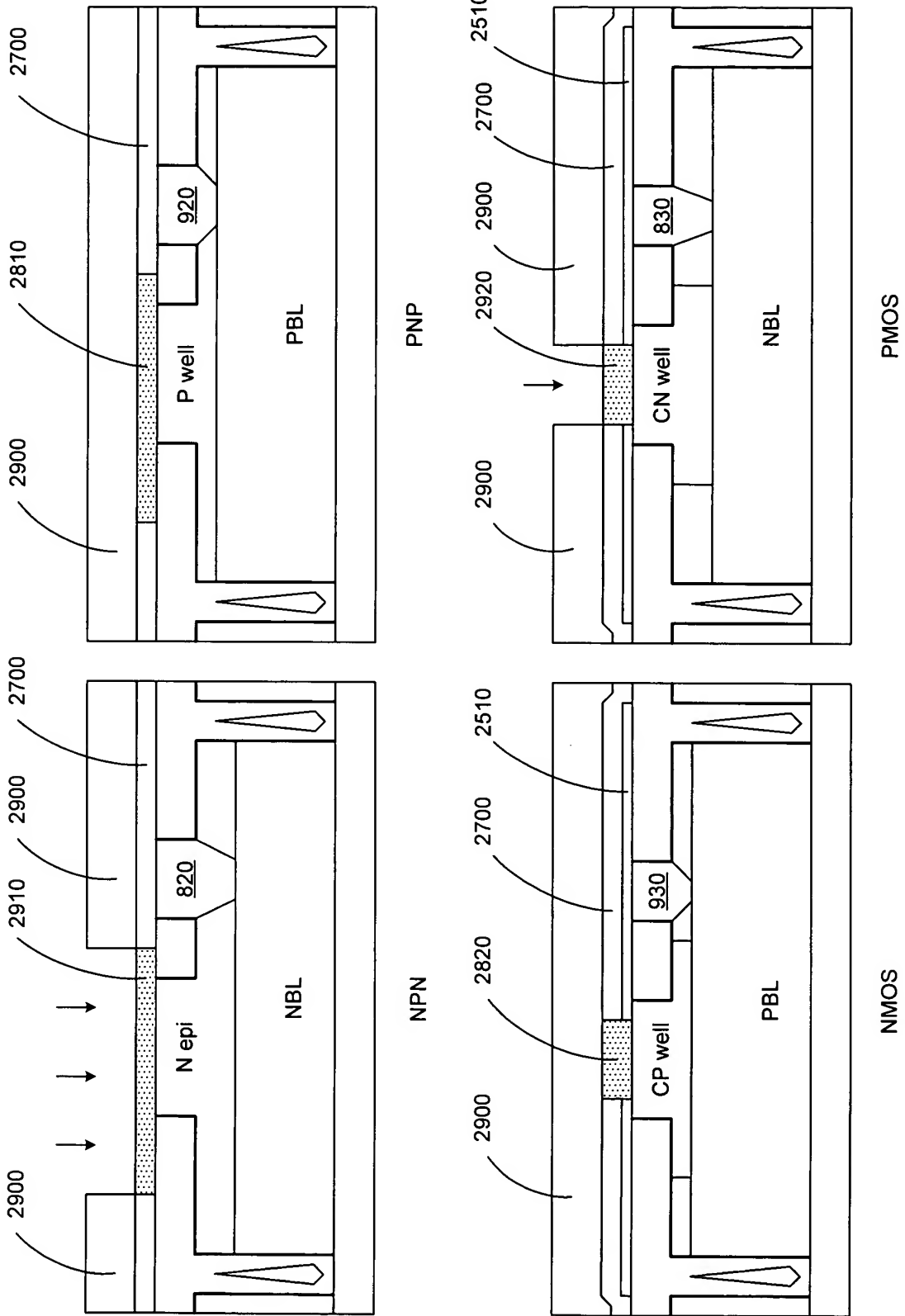


FIGURE 29

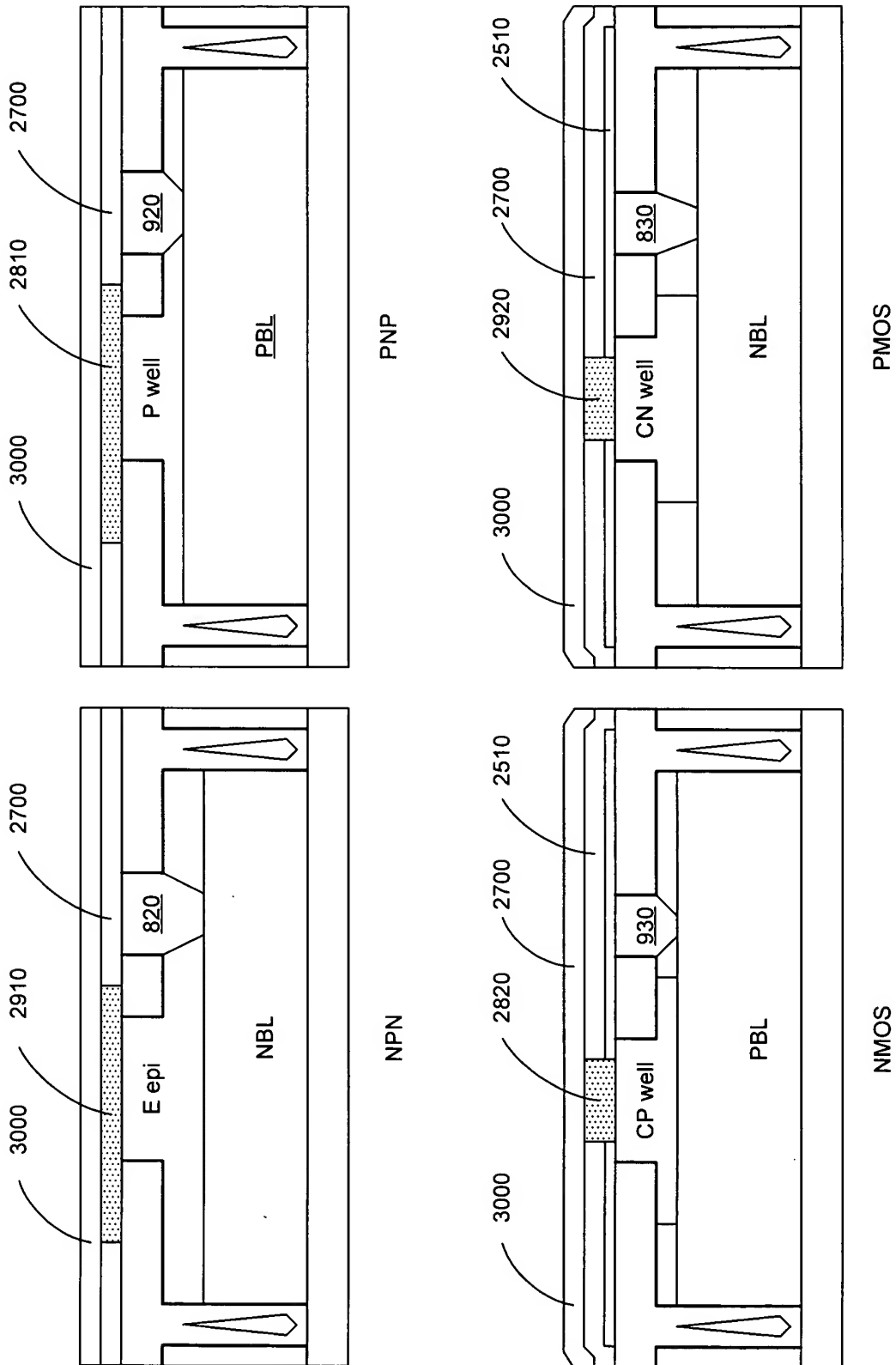


FIGURE 30

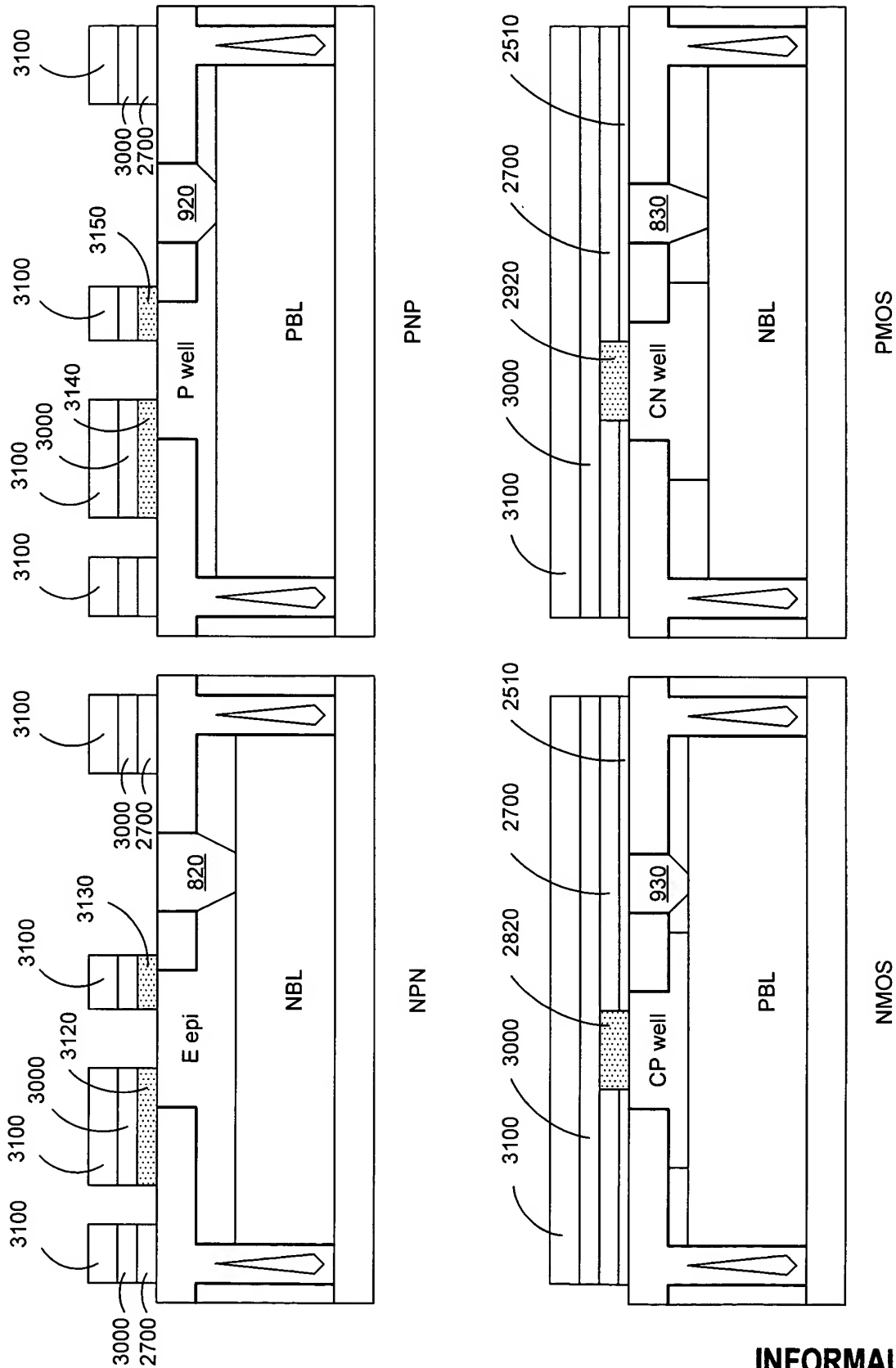


FIGURE 31

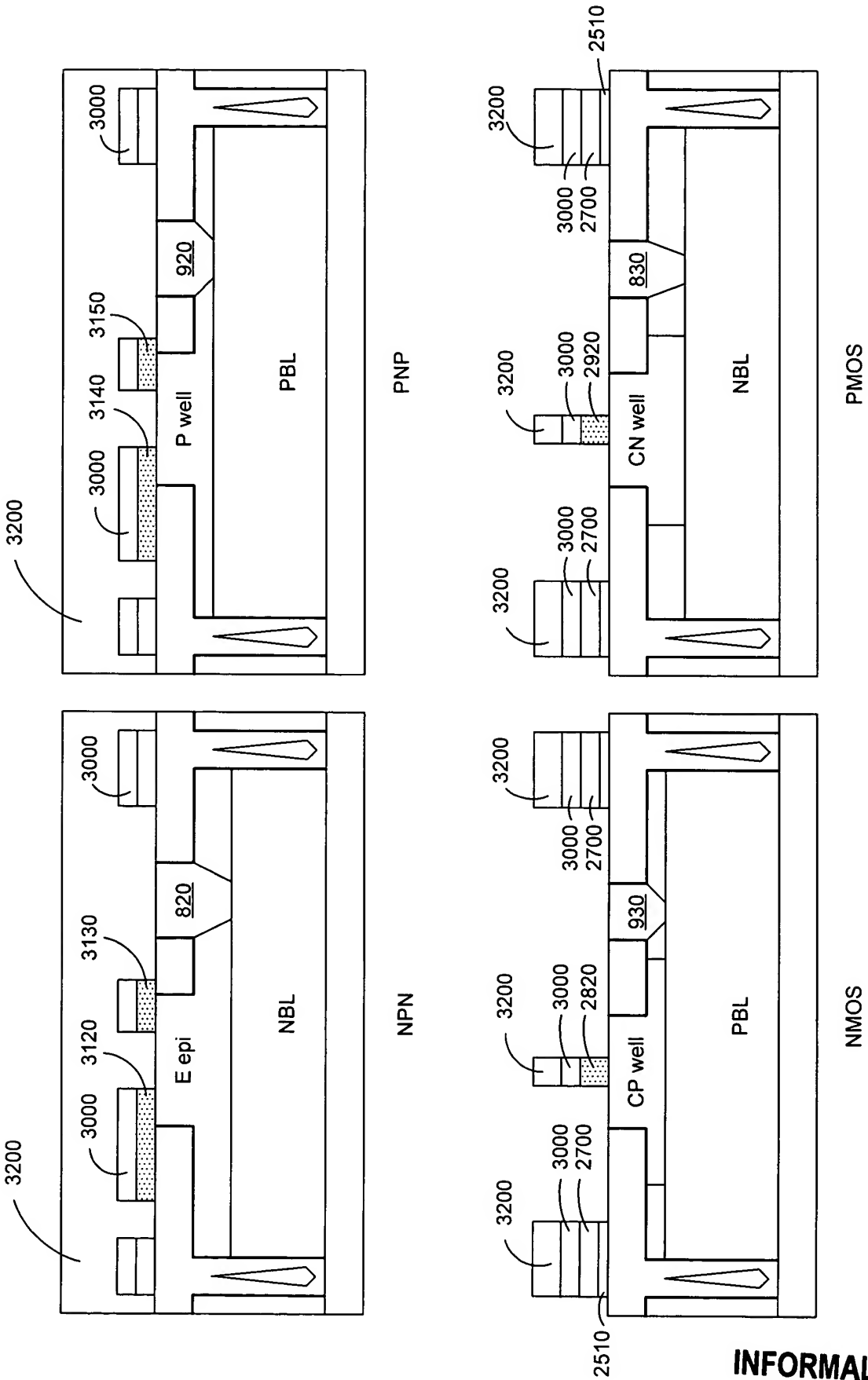
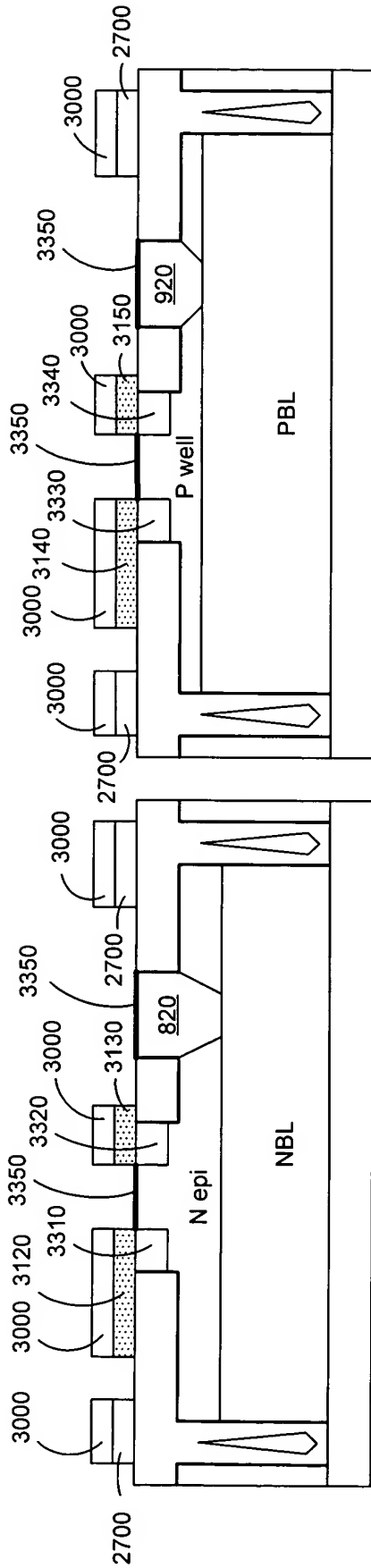
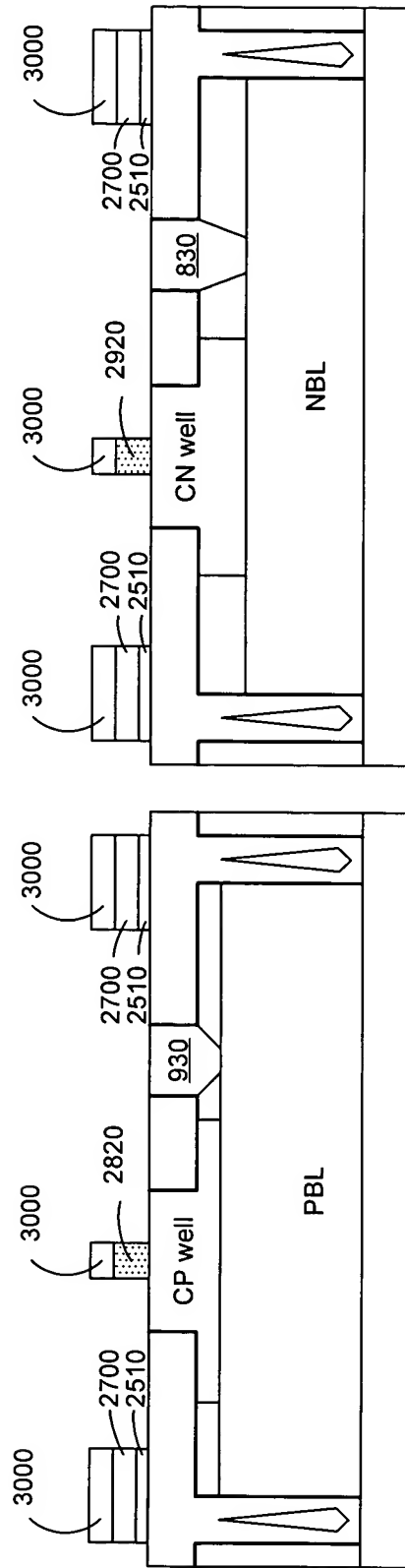


FIGURE 32



NPN

PNP



NMOS

PMOS

FIGURE 33

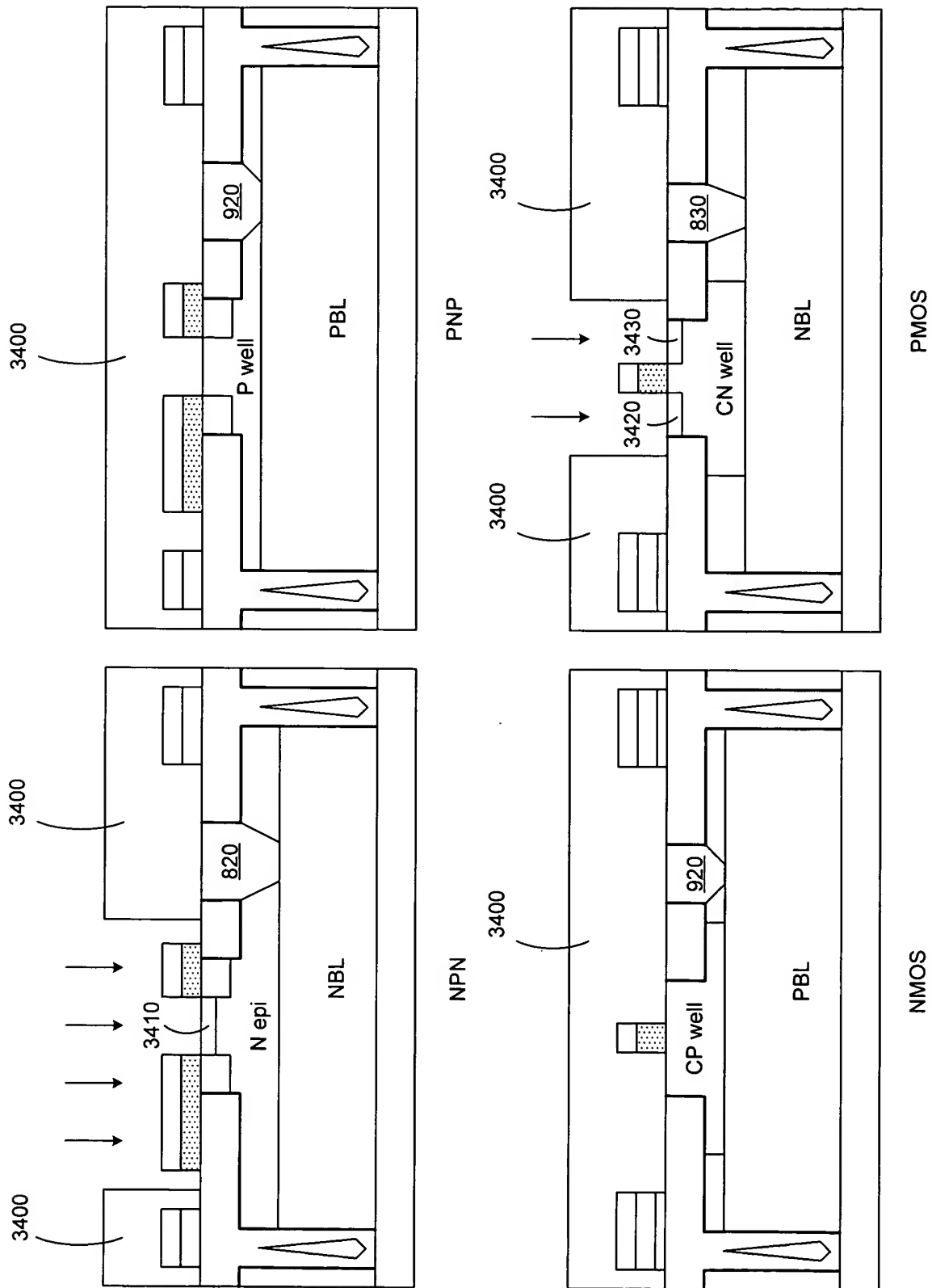


FIGURE 34

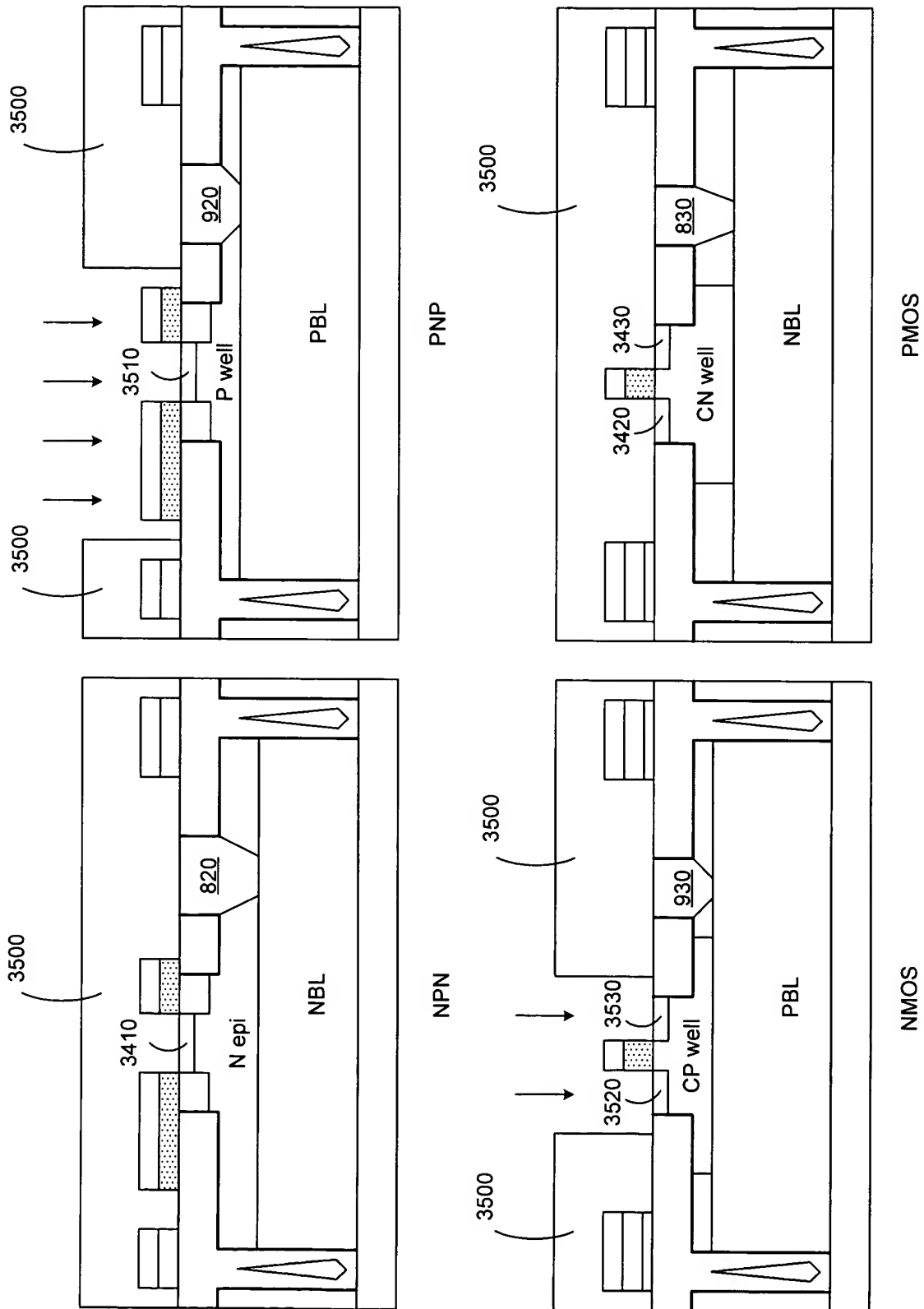


FIGURE 35

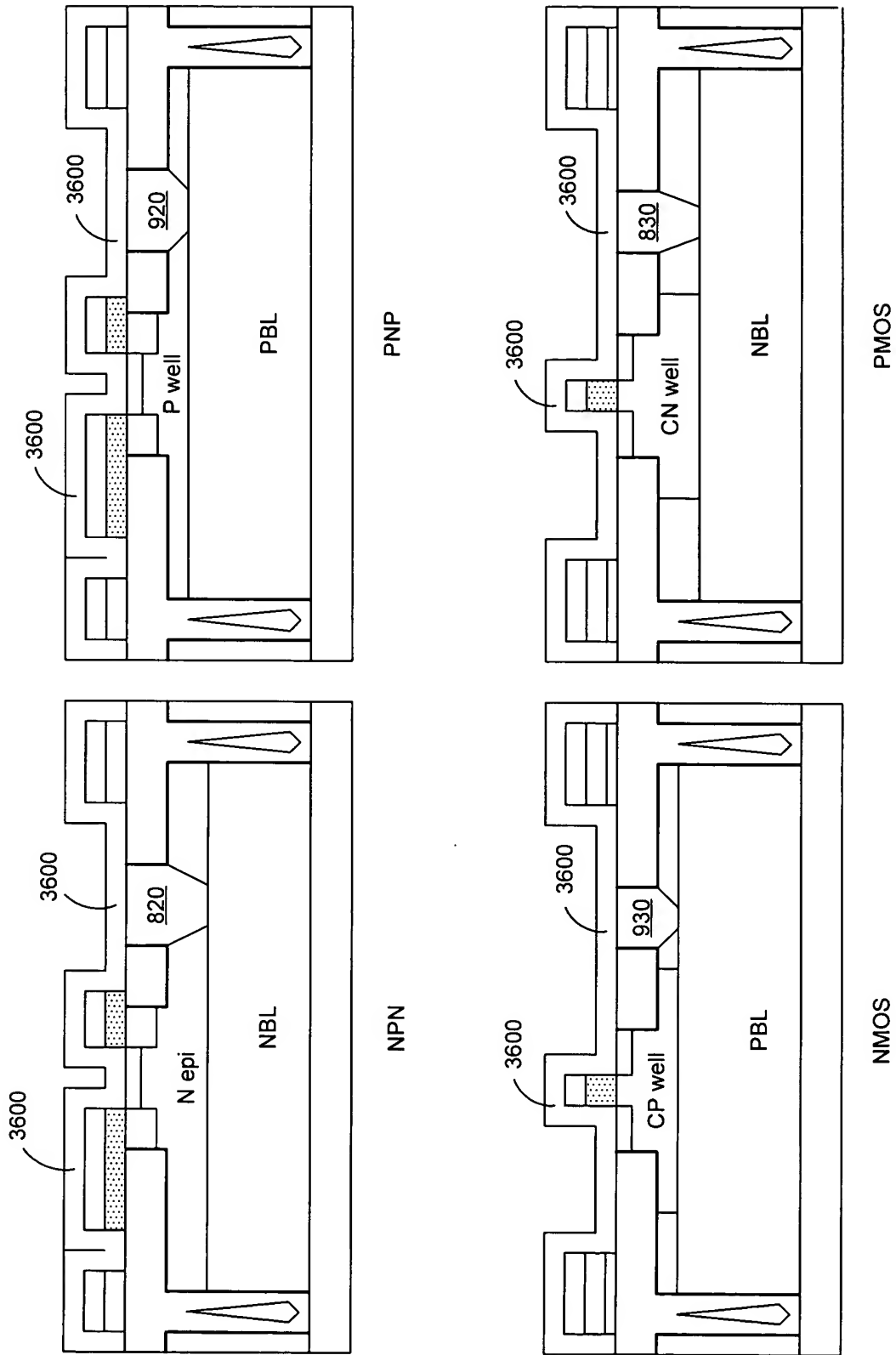


FIGURE 36

INFORMAL DRAWINGS

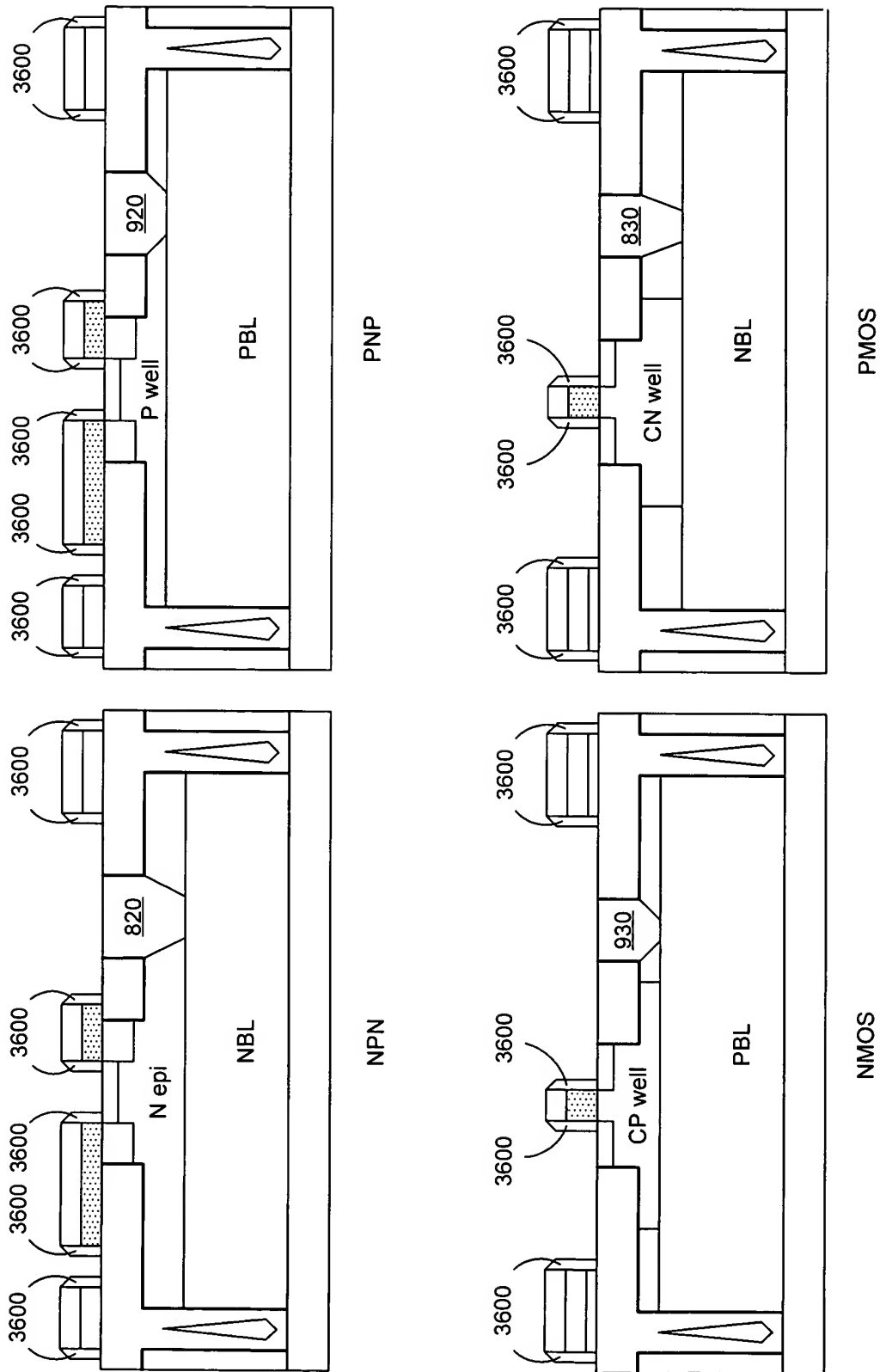


FIGURE 37

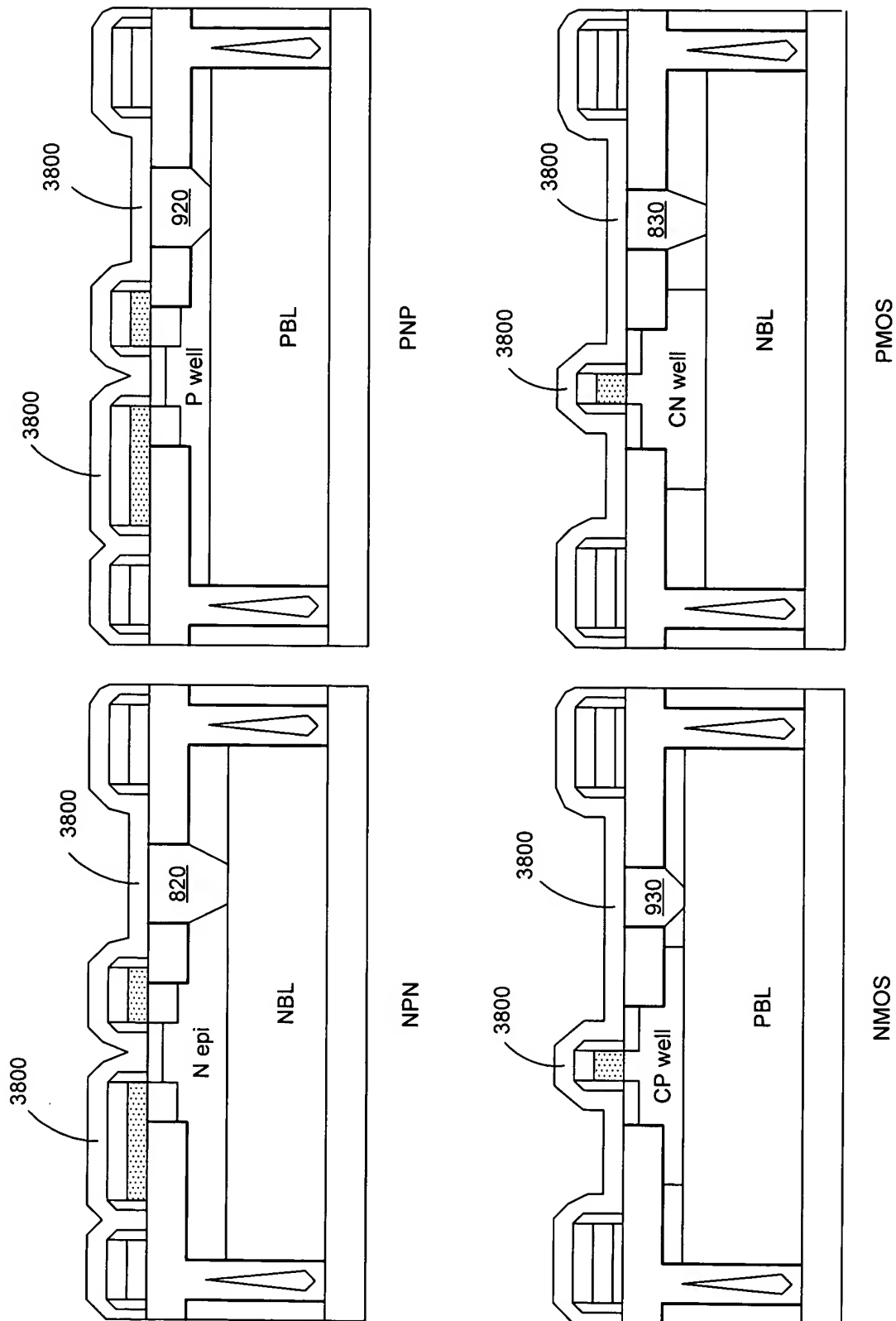


FIGURE 38

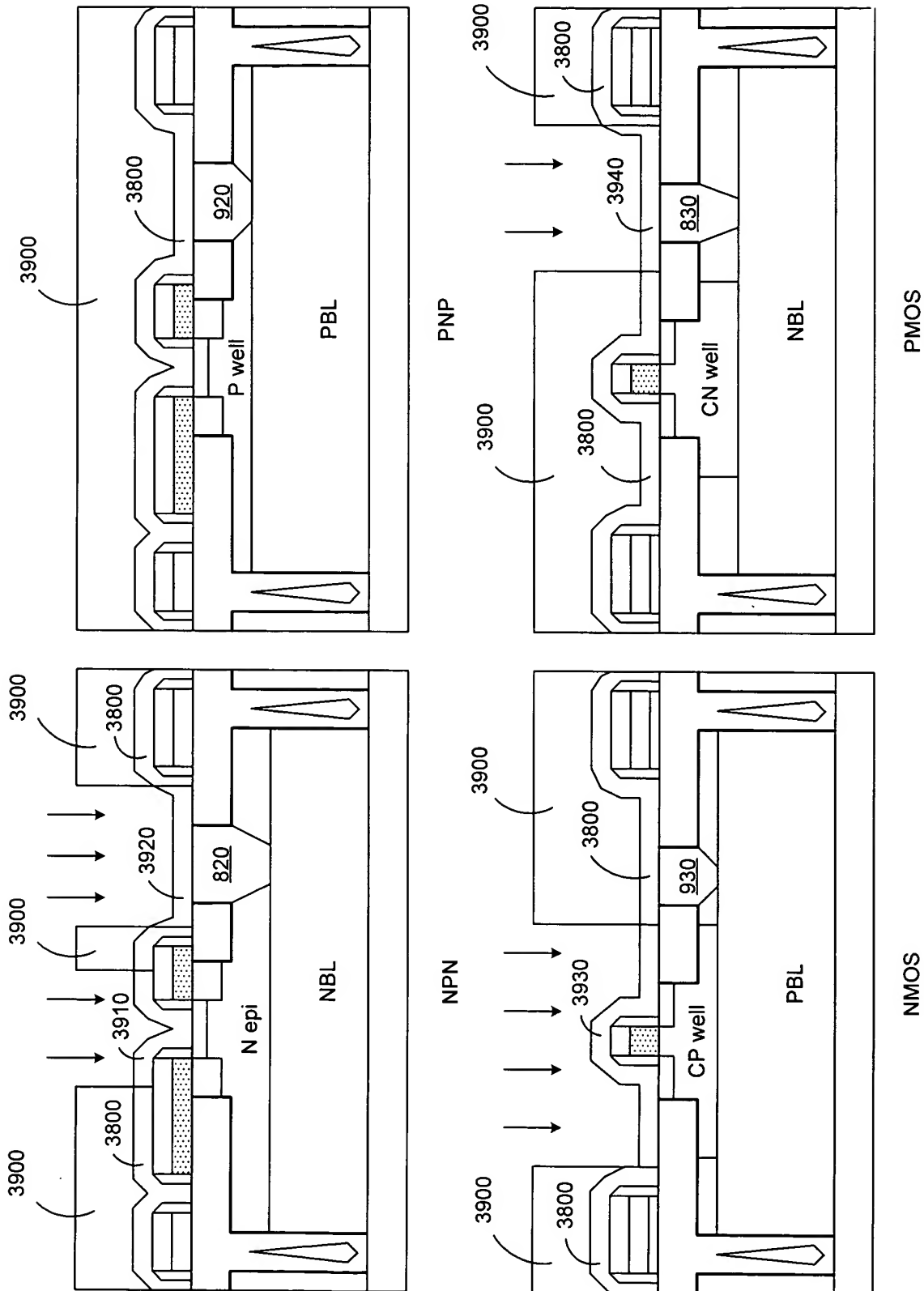


FIGURE 39

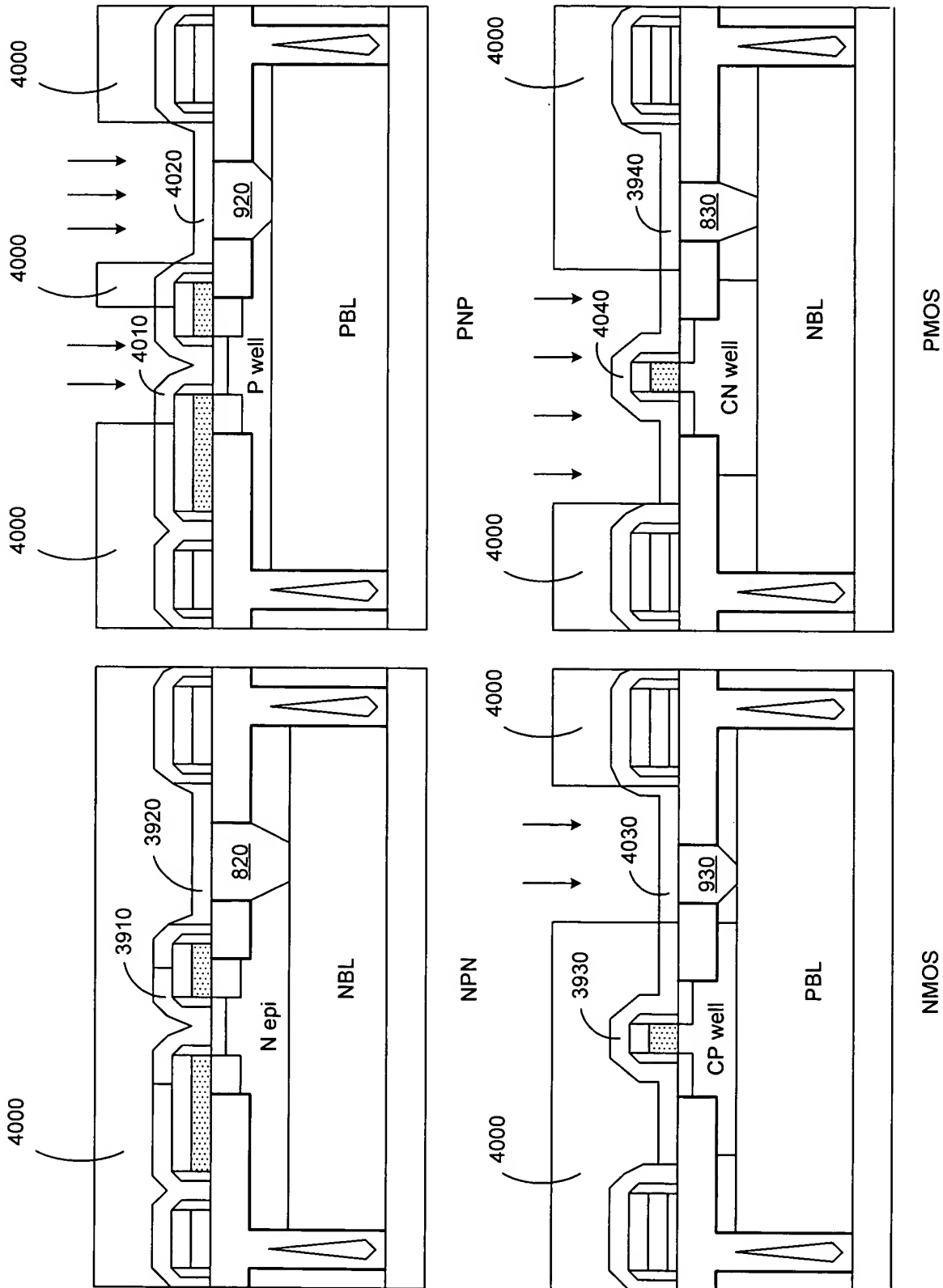


FIGURE 40

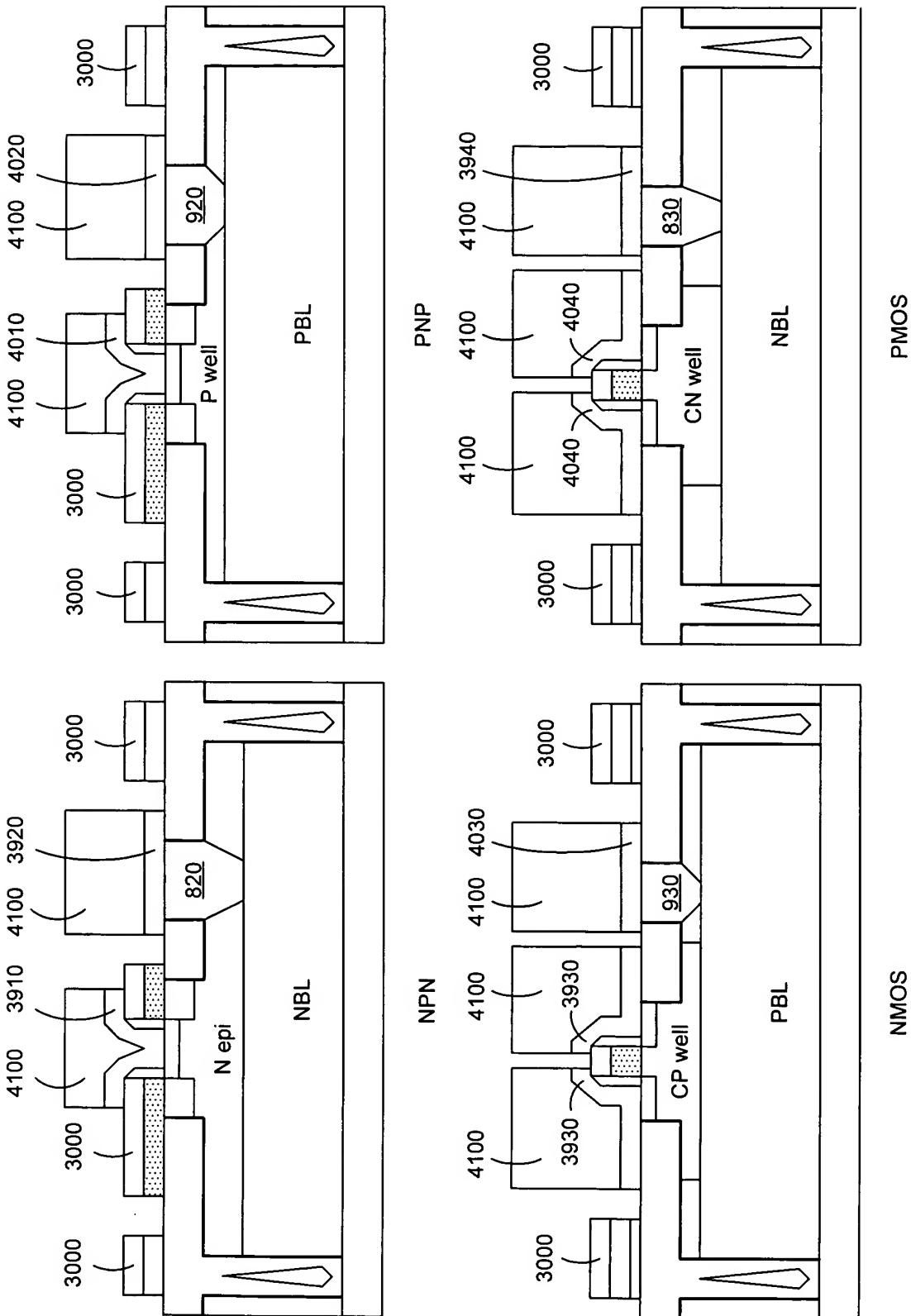


FIGURE 41

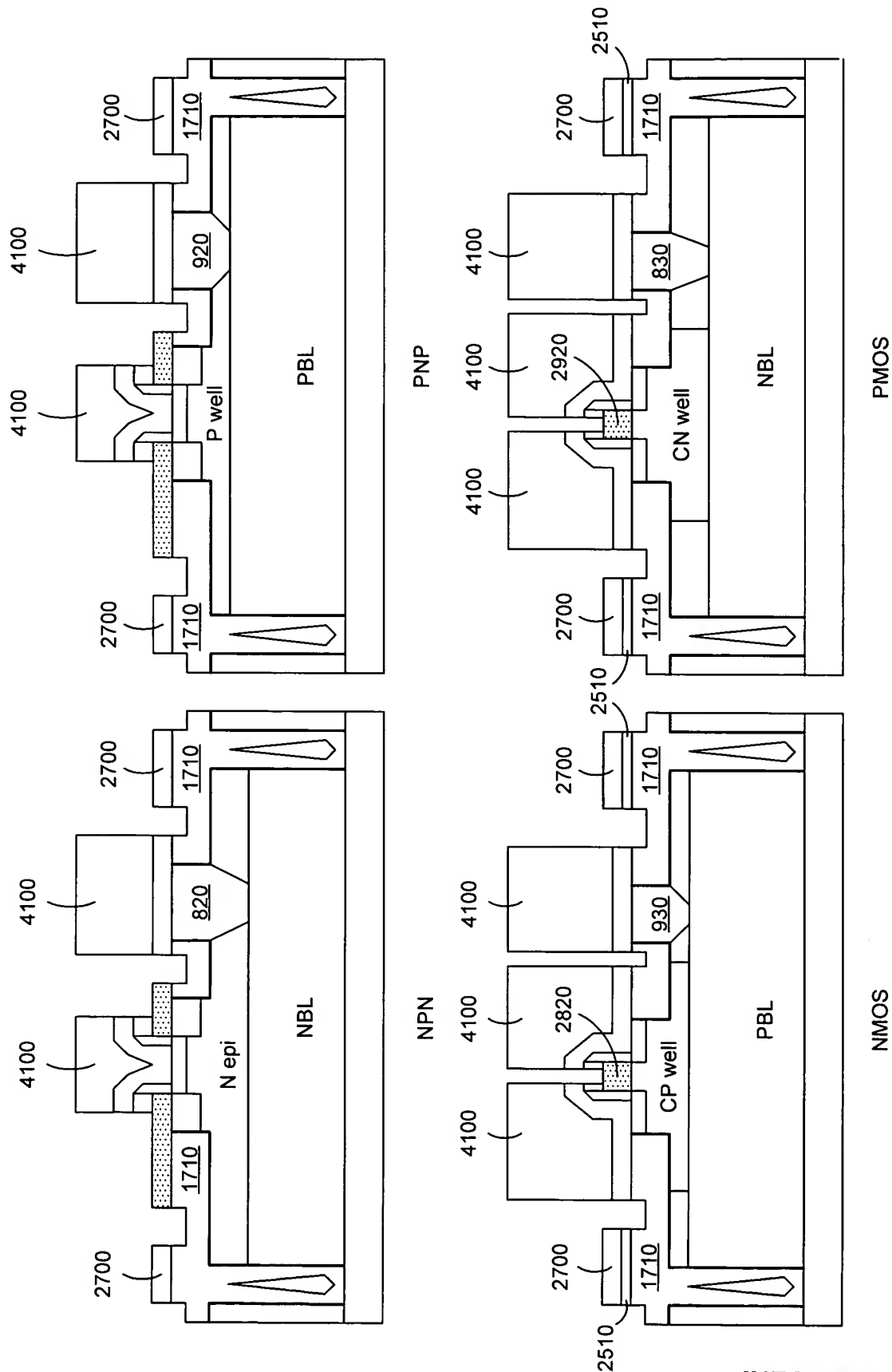


FIGURE 42

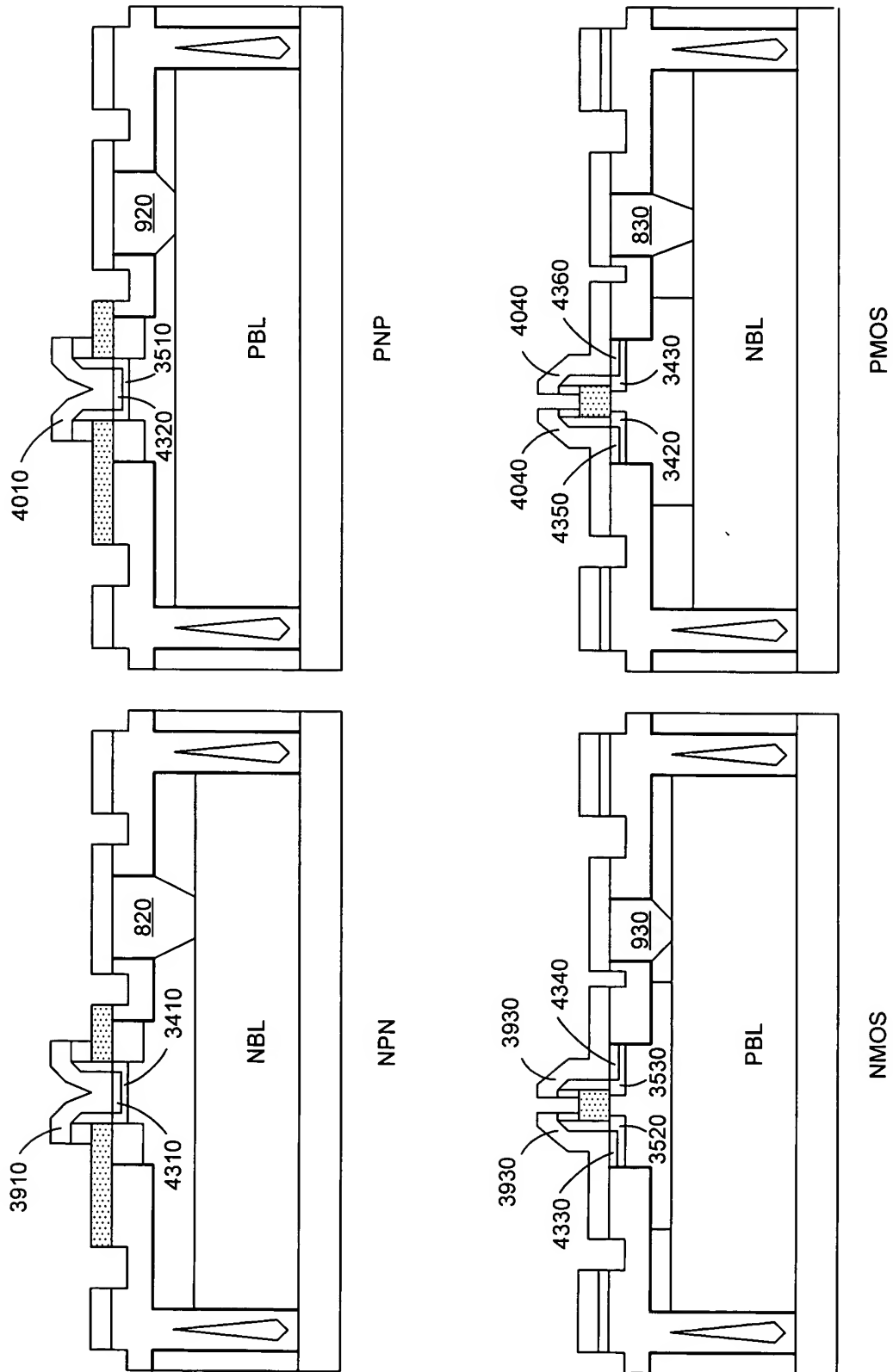


FIGURE 43

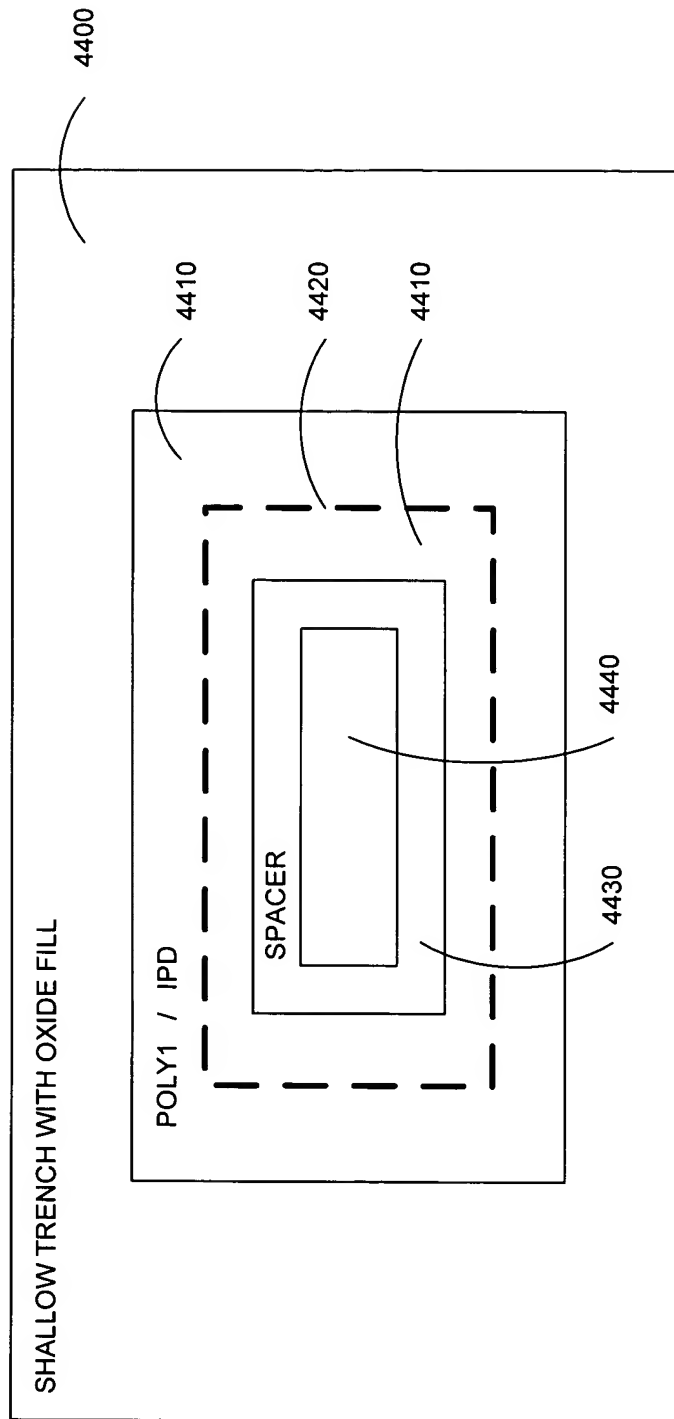


FIGURE 44

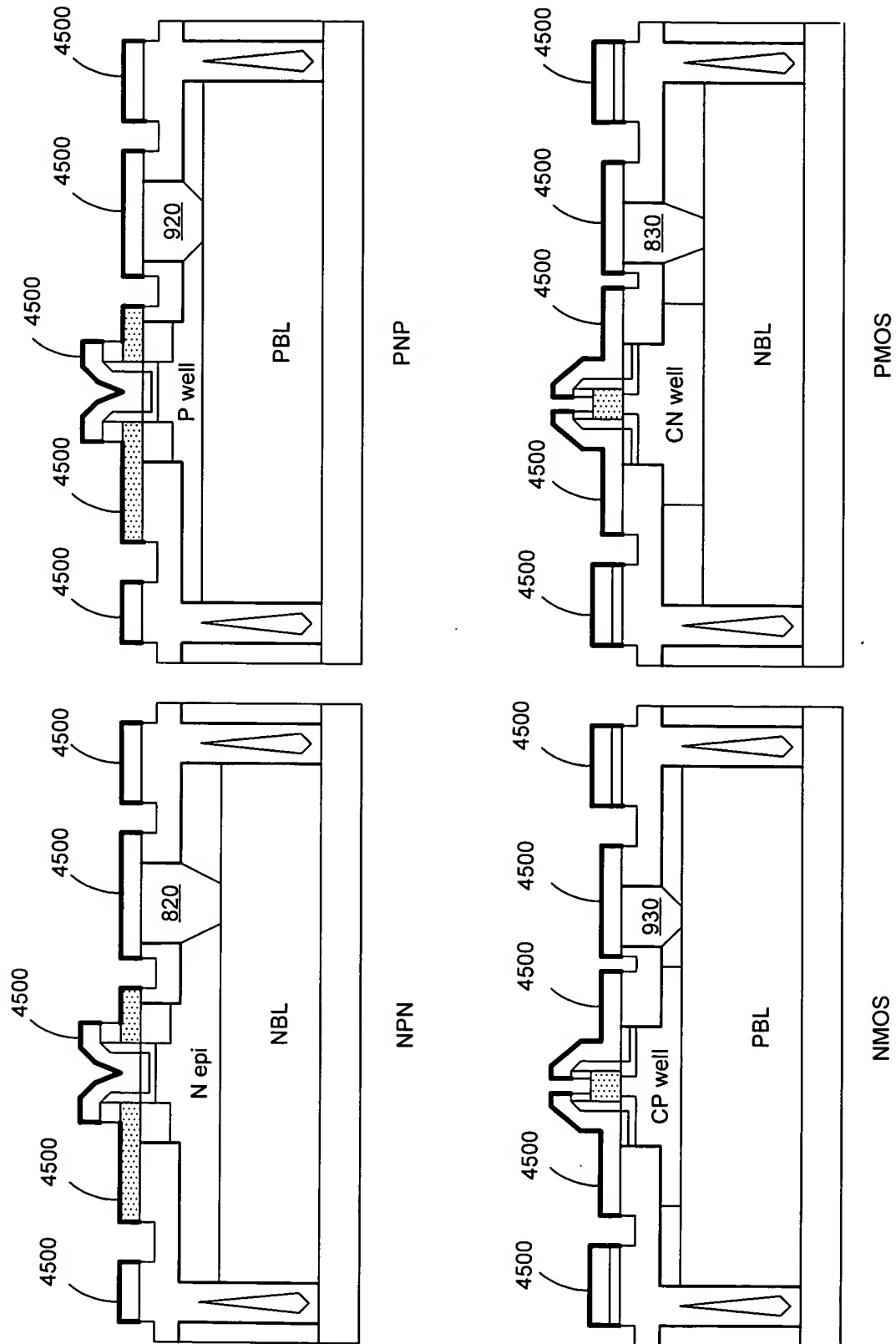


FIGURE 45

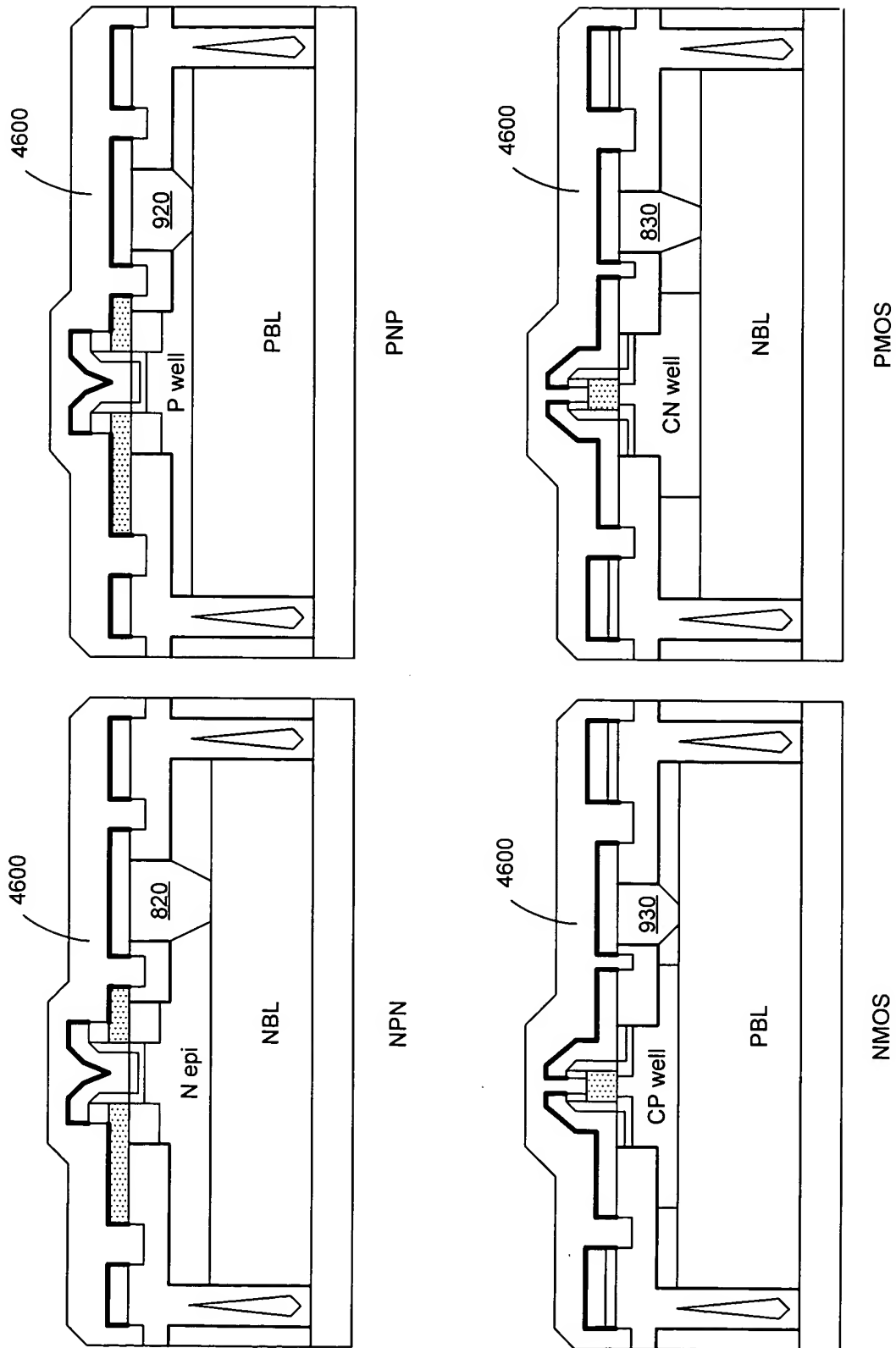


FIGURE 46

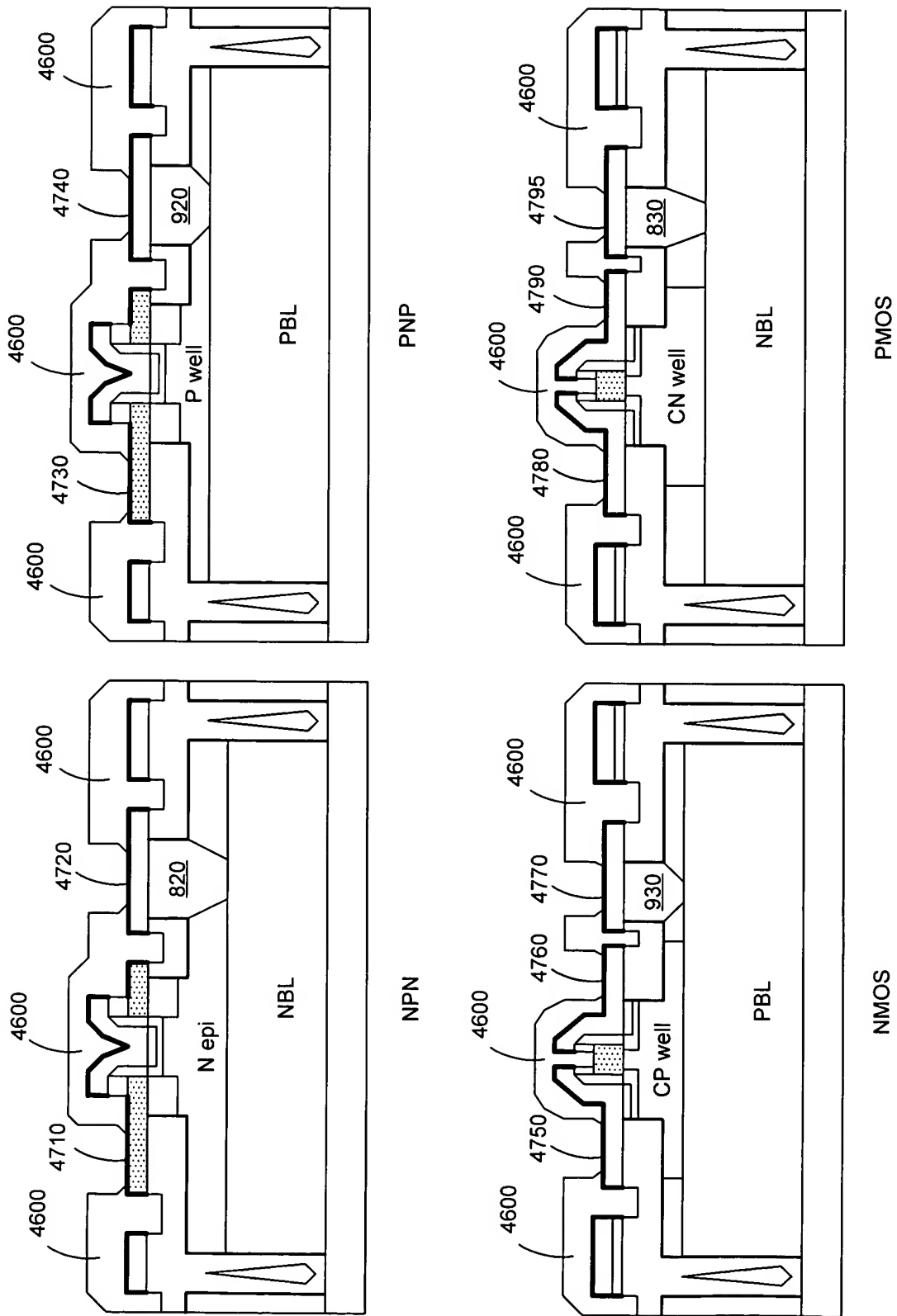


FIGURE 47

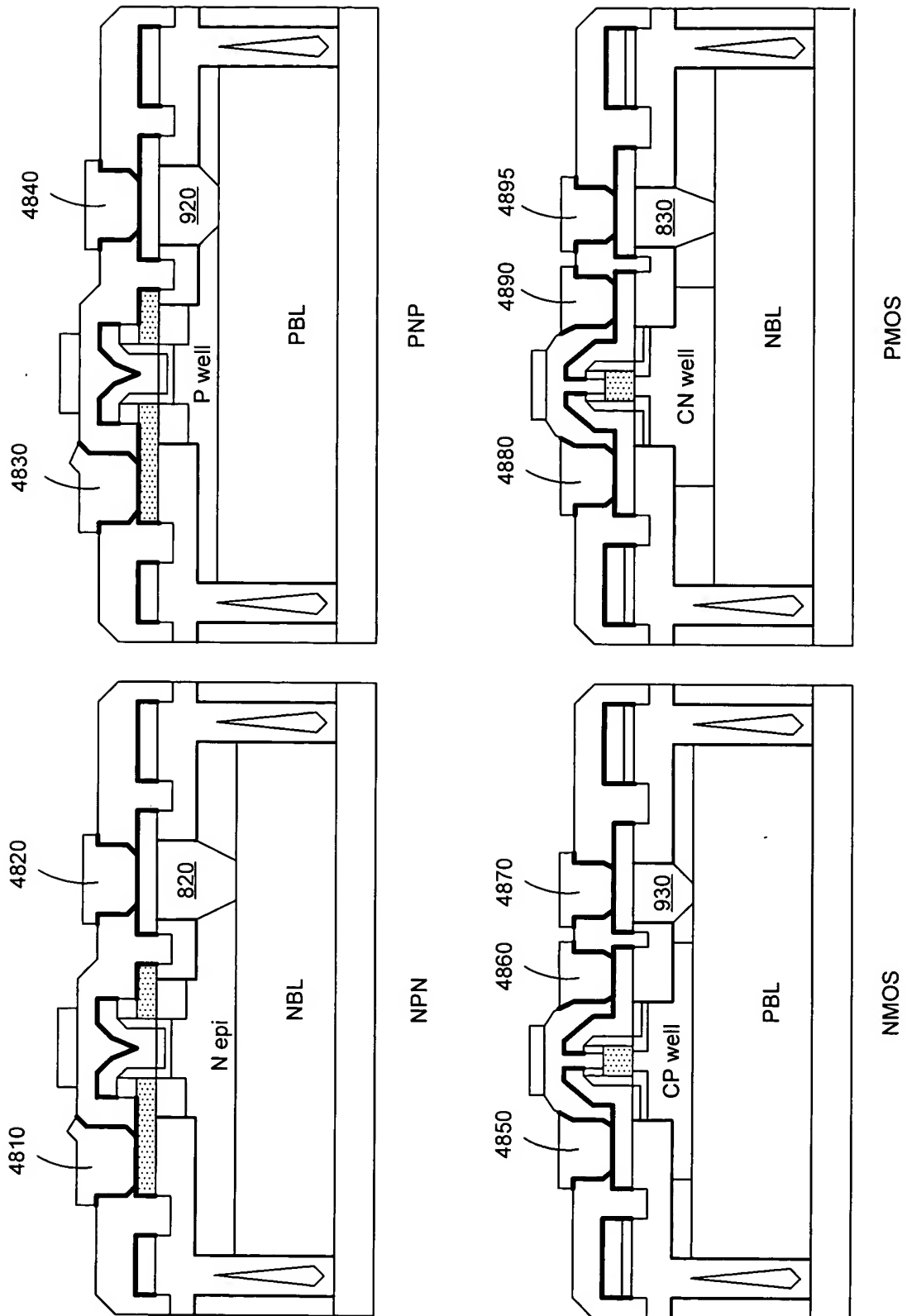


FIGURE 48

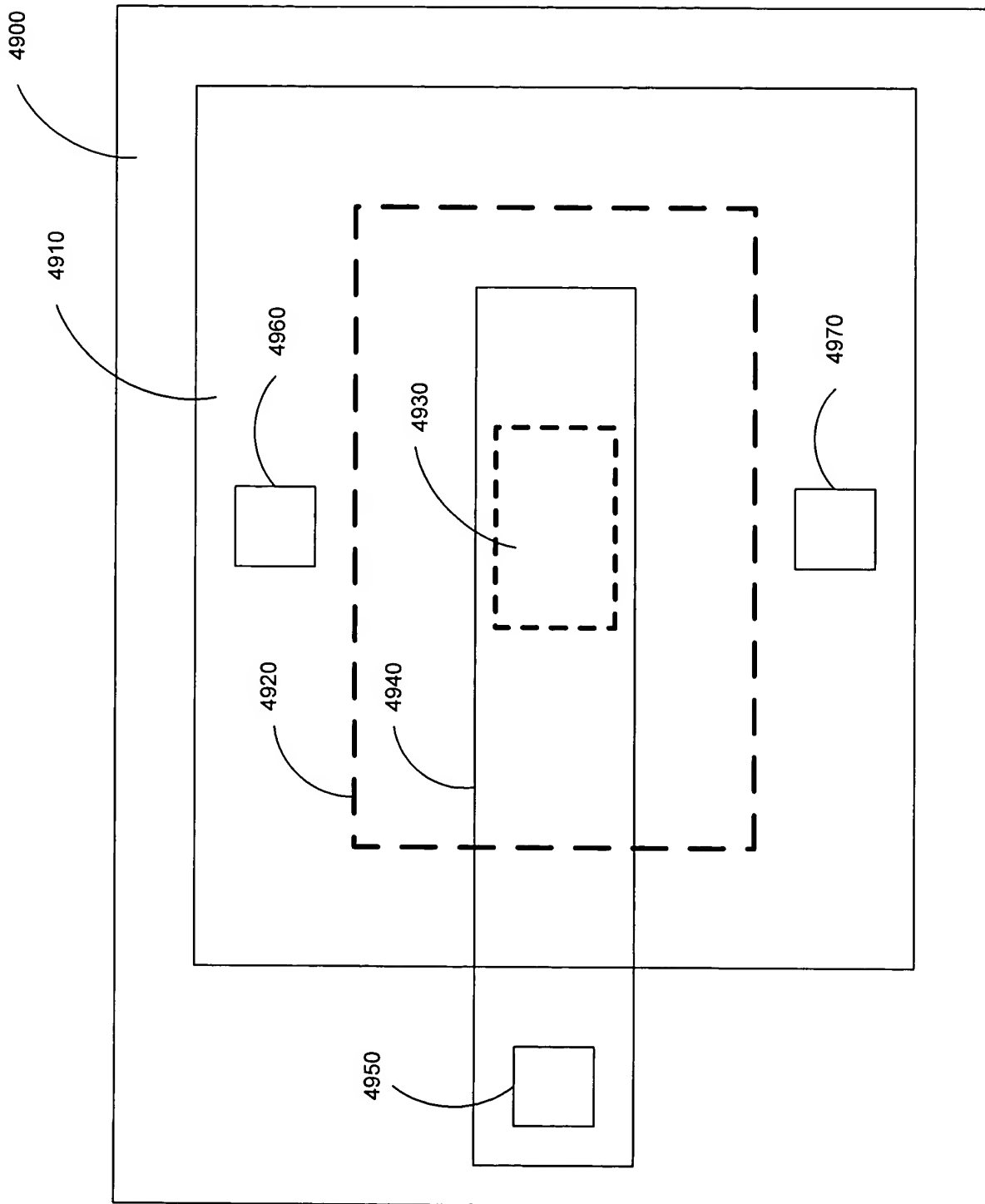


FIGURE 49

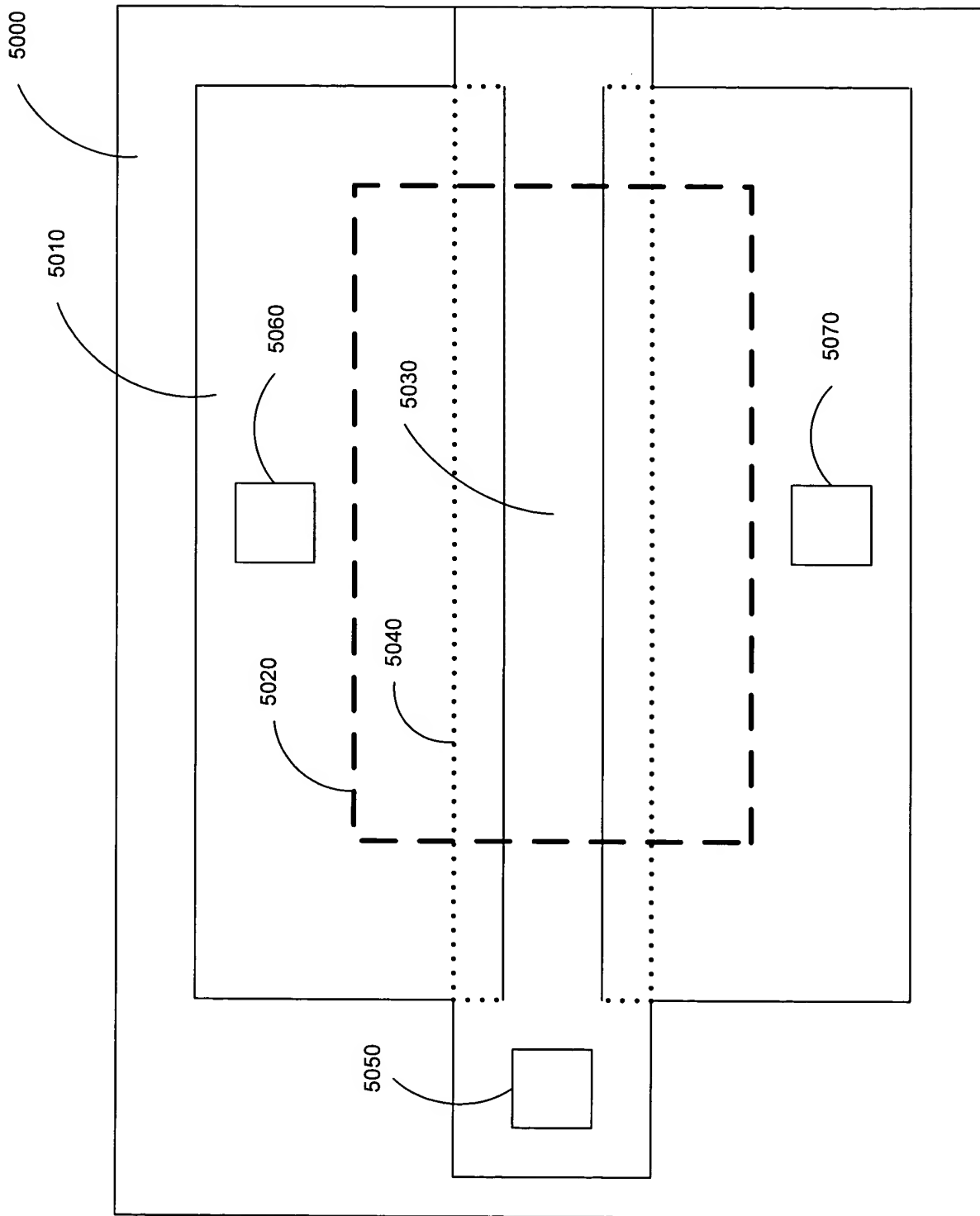


FIGURE 50